This presentation will start with an overview of power delivery networks (PDN) in die, package, and PCB structures. The fundamental concepts will be explained from intuitive and analytical perspectives. The attention will then focus on the specifics of power delivery in FPGA applications and in particular the selection of power supply decoupling capacitors. Optimization of decoupling capacitors through on-die measurement of PDN impedance frequency profile is performed by configuring and using only logic blocks commonly available in existing FPGAs without the need of external test instruments. A case study implemented in a Spartan 3A FPGA has been able to measure the PDN impedance frequency profile at frequencies up to 500MHz and has identified resonance peaks not seen by board level measurements. Further application of the on-die PDN impedance measurement to troubleshooting power integrity issues will be presented and case studies will be analyzed. The presentation will conclude with a discussion of challenges in power delivery network design and validation in advanced multi-die, silicon interposer, and stacked-die 3D FPGAs.

Cosmin Iorga is a Principal Design Engineer at Inphi Corporation and a part time instructor at UCLA Extension. Cosmin brings combined expertise in CMOS and BiCMOS high-speed analog integrated circuit design, signal integrity, power integrity, and noise coupling suppression at die, package, and board levels, with more than 20 years of experience. Cosmin has earned his Ph.D. in Electrical Engineering from Stanford University, and he holds nine patents covering innovative solutions in circuit design, noise coupling reduction, and signal integrity. Cosmin is the author of the book “Noise Coupling in Integrated Circuits: A Practical Approach to Analysis, Modeling, and Suppression.”
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