



Quantum Computing with Microwaves

Joe Bardin
UMass Amherst & Google Quantum AI
MTT-S Webinar
04.13.2021

UMass
Amherst





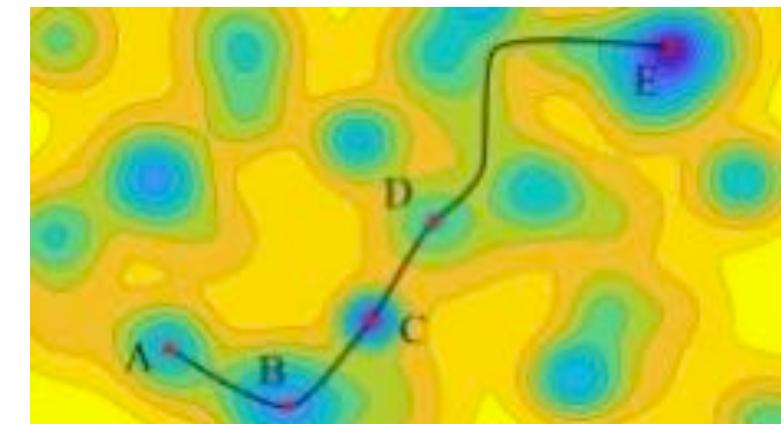
“... if you want to make a simulation of nature, you'd better make it quantum mechanical, and by golly, it's a wonderful problem because **it doesn't look so easy.**” - Richard Feynman, 1982



Cracking RSA Encryption

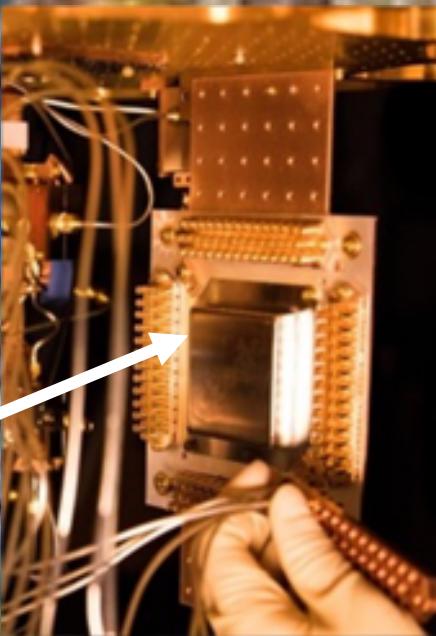
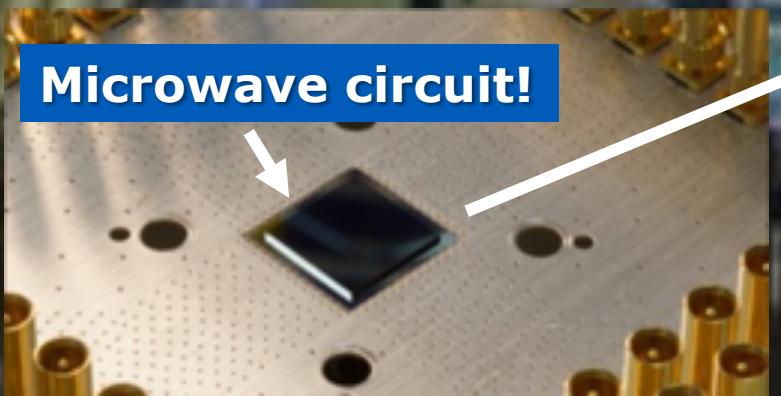


Quantum chemistry



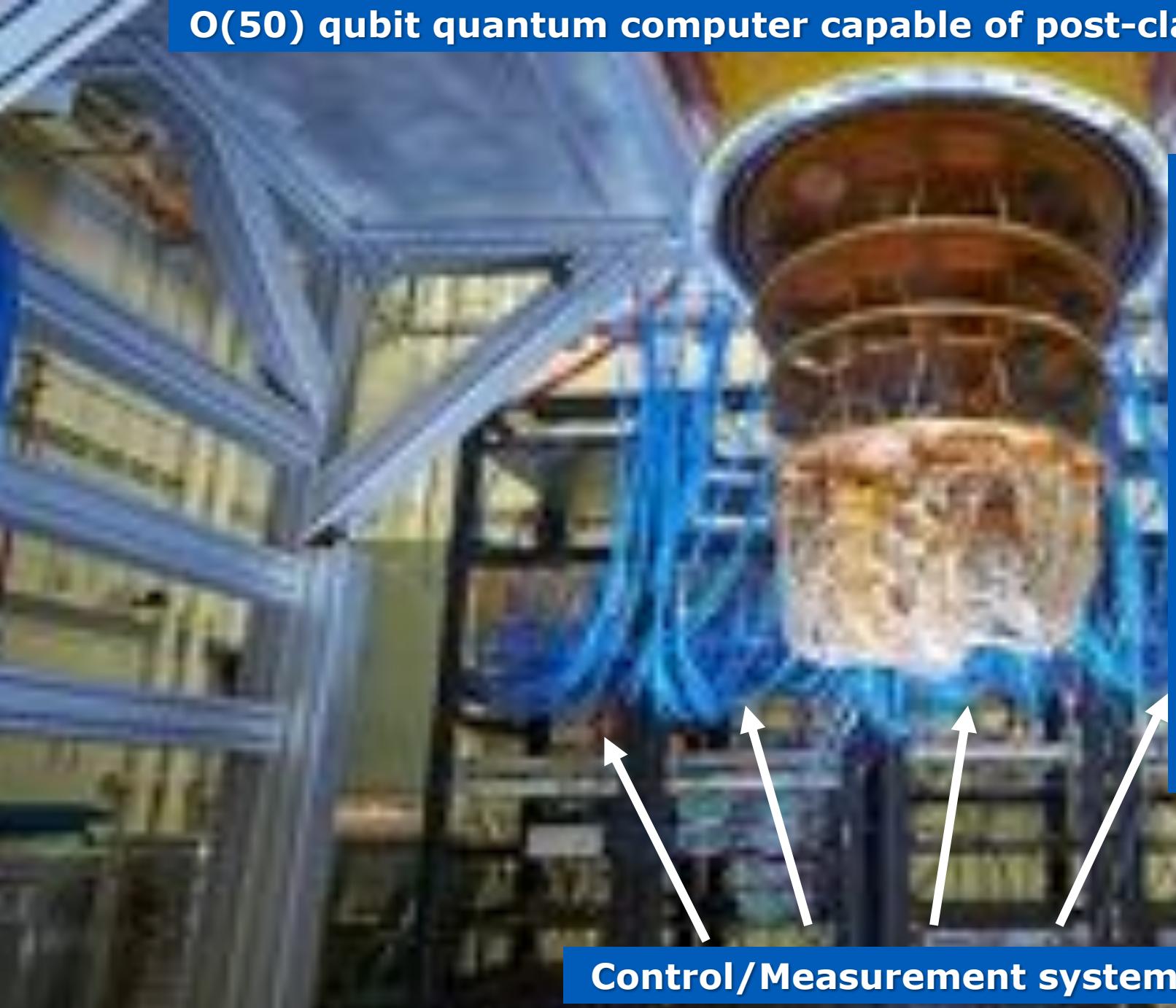
Optimization

O(50) qubit quantum computer capable of post-classical computation



Quantum processor @ 10mK

O(50) qubit quantum computer capable of post-classical computation



Control/measurement requires:

- **60 RF AWG**
- **150 Baseband AWG**
- **10 RF Digitizers**
- **10 InP Cryo LNAs**
- **10 Parametric LNAs**
- **Hundreds of RF attenuators**
- **Hundreds of RF filters**
- **50 Cryo circulators**
- **Hundreds of RF Cables**

Control/Measurement system

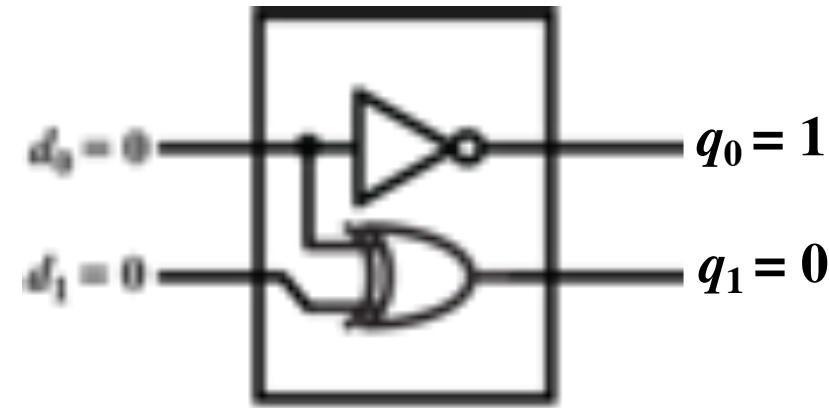
Quantum computers are enabled
by microwave technology!

Agenda: four key questions

1. What is quantum computing?
2. How do we build quantum computers?
3. What can today's quantum computers do?
4. What microwave engineering is required for the future?

Classical Computing

Increment by 1



$$q_0 = 0$$

$$q_1 = 1$$

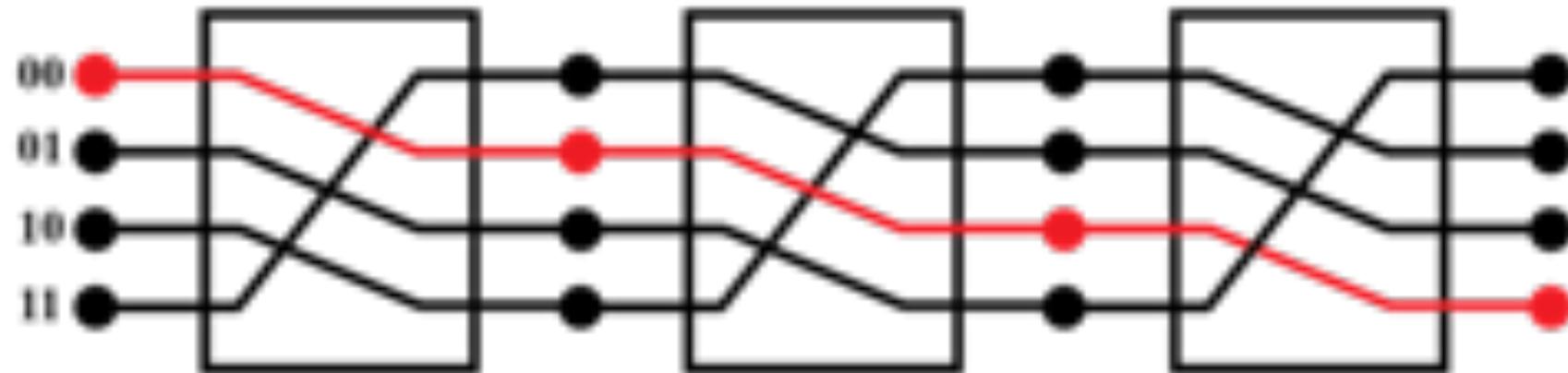
$d_1 d_0$	$q_1 q_0$
0 0	0 1
0 1	1 0
1 0	1 1
1 1	0 0

Classical Computing

Key Properties

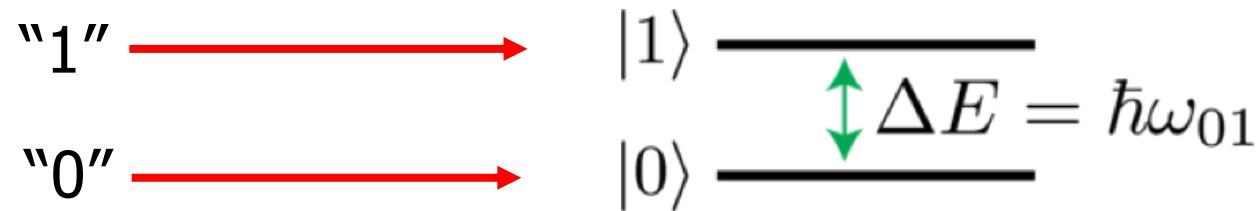
- Each input bitstring maps to just one output bitstring
- Measurement and fanout are trivial
- Any possible classical operation realized using just NAND

$d_1 d_0$	$q_1 q_0$
0 0	0 1
0 1	1 0
1 0	1 1
1 1	0 0



Properties of Quantum Bits

Qubit = quantum mechanical system w/two energy levels



Quantum mechanics: state fully described by wave function

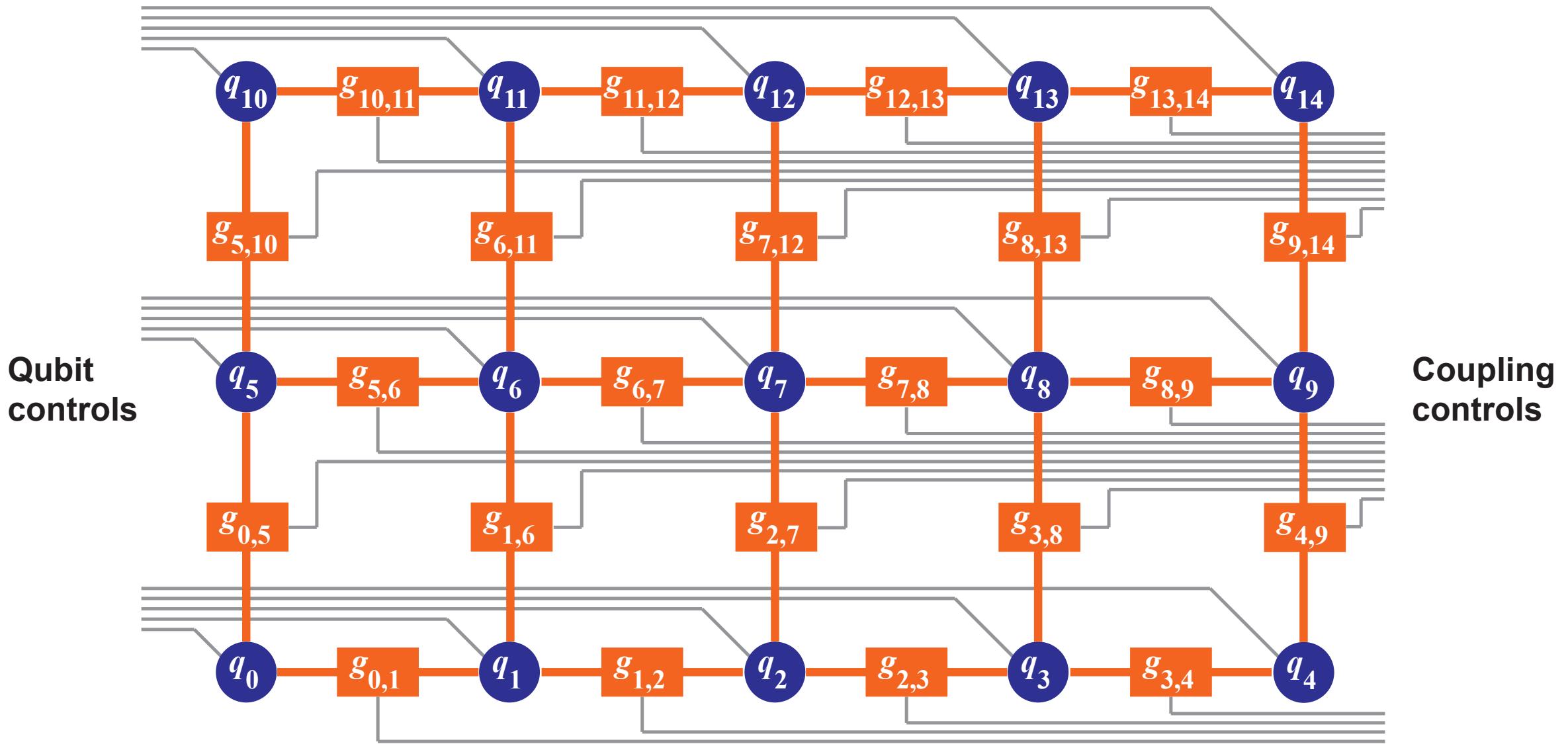
$$|\psi\rangle = \alpha_0 |0\rangle + \alpha_1 |1\rangle = \begin{bmatrix} \alpha_0 \\ \alpha_1 \end{bmatrix} \quad \text{Complex unit state-vector.}$$

Quantum mechanics: measurement of qubit causes a probabilistic state collapse and returns single classical bit

$$P_{|0\rangle} = |\alpha_0|^2$$

$$P_{|1\rangle} = |\alpha_1|^2$$

Quantum Processor = Array of Coupled Qubits



Properties of Quantum Processors

- N qubit quantum processor has 2^N eigenstates (bitstrings):

$$|\psi\rangle = \alpha_{00\dots 0} |00\dots 0\rangle + \alpha_{00\dots 1} |00\dots 1\rangle + \dots + \alpha_{11\dots 1} |11\dots 1\rangle = \begin{bmatrix} \alpha_{00\dots 0} \\ \alpha_{00\dots 1} \\ \vdots \\ \alpha_{11\dots 1} \end{bmatrix}$$

complex probability amplitudes: degrees of freedom

- Measurement has same interpretation as single qubit case, e.g.,

$$P_{|00\dots 0\rangle} = |\alpha_{00\dots 0}|^2$$

Quantum Computing

Qubit 0: 00

Qubit 1: 00

$|00\rangle$ 

$|00\rangle$ 

$|00\rangle$ 

$|00\rangle$ 

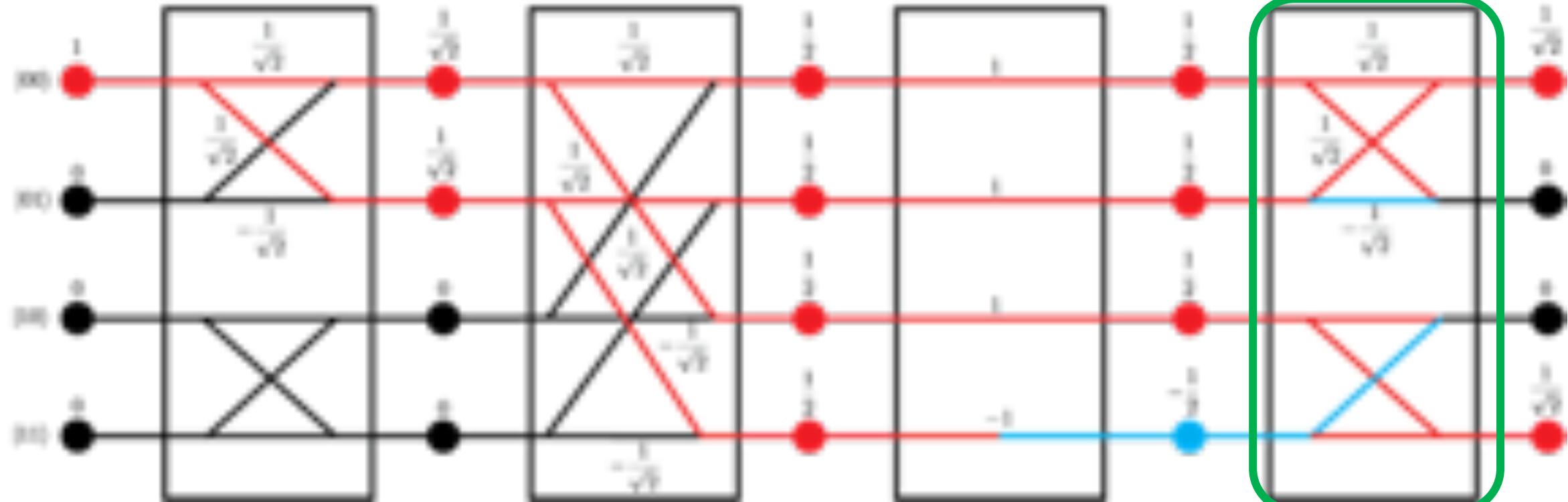
Key Ingredients

- Unitary gate operations
- Superposition
- Entanglement
- Interference
- Projective measurement

ting



$$\hat{U}_{H,0} = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix} \otimes \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$$
$$= \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 & 0 & 0 \\ 1 & -1 & 0 & 0 \\ 0 & 0 & 1 & 1 \\ 0 & 0 & 1 & -1 \end{bmatrix}$$



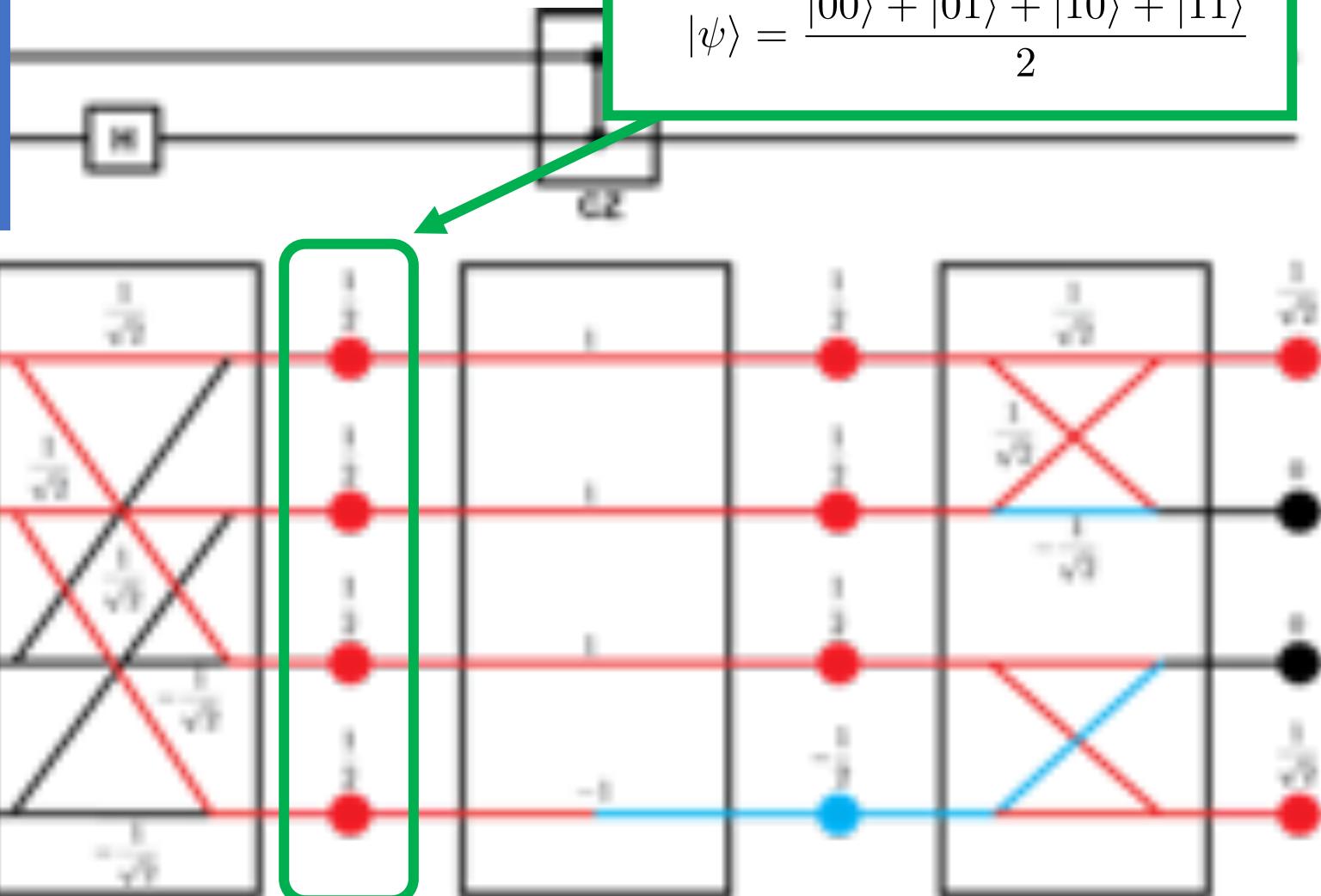
Key Ingredients

- Unitary gate operations
- Superposition
- Entanglement
- Interference
- Projective measurement

ting

Coherent superposition:

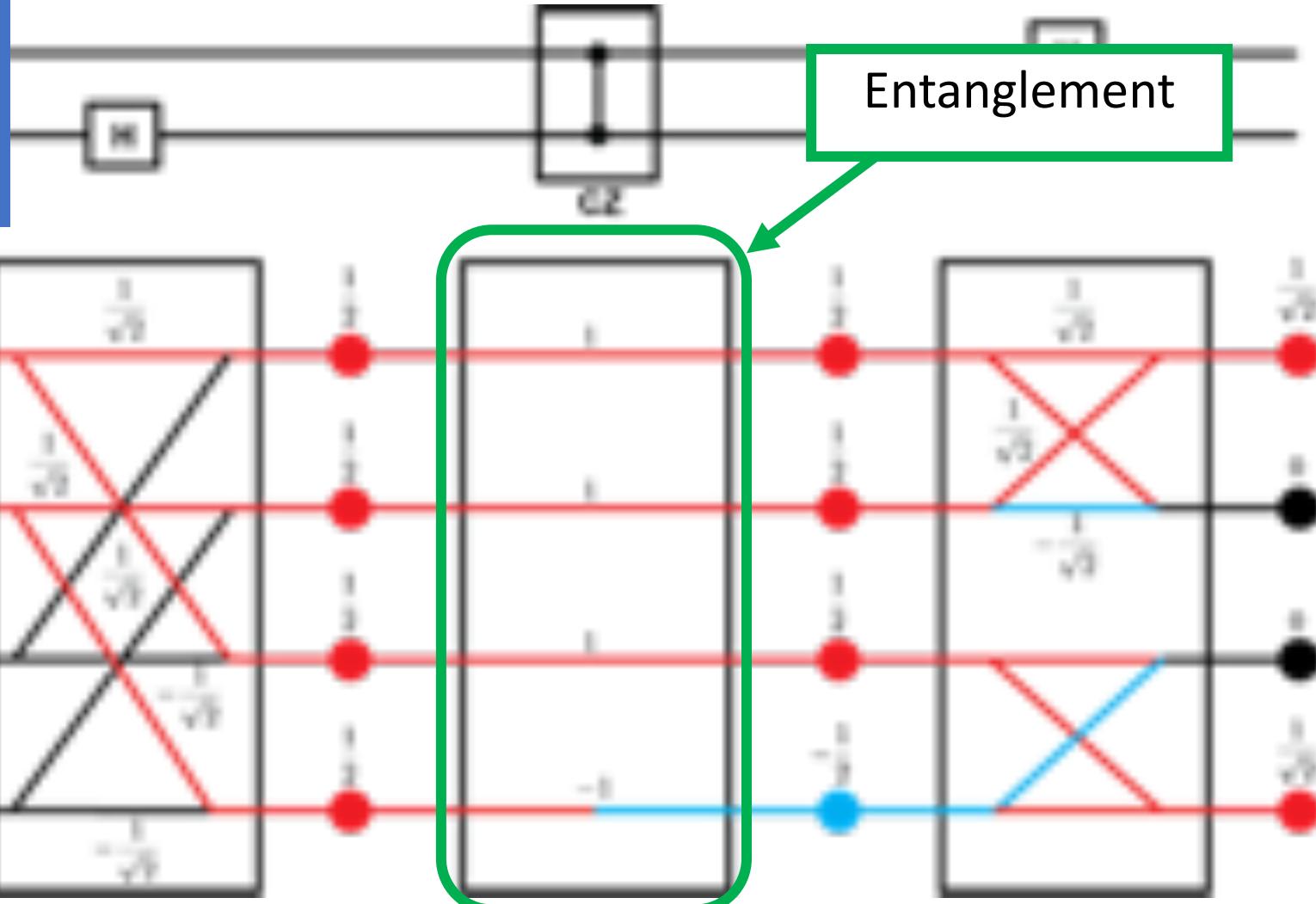
$$|\psi\rangle = \frac{|00\rangle + |01\rangle + |10\rangle + |11\rangle}{2}$$



Key Ingredients

- Unitary gate operations
- Superposition
- Entanglement
- Interference
- Projective measurement

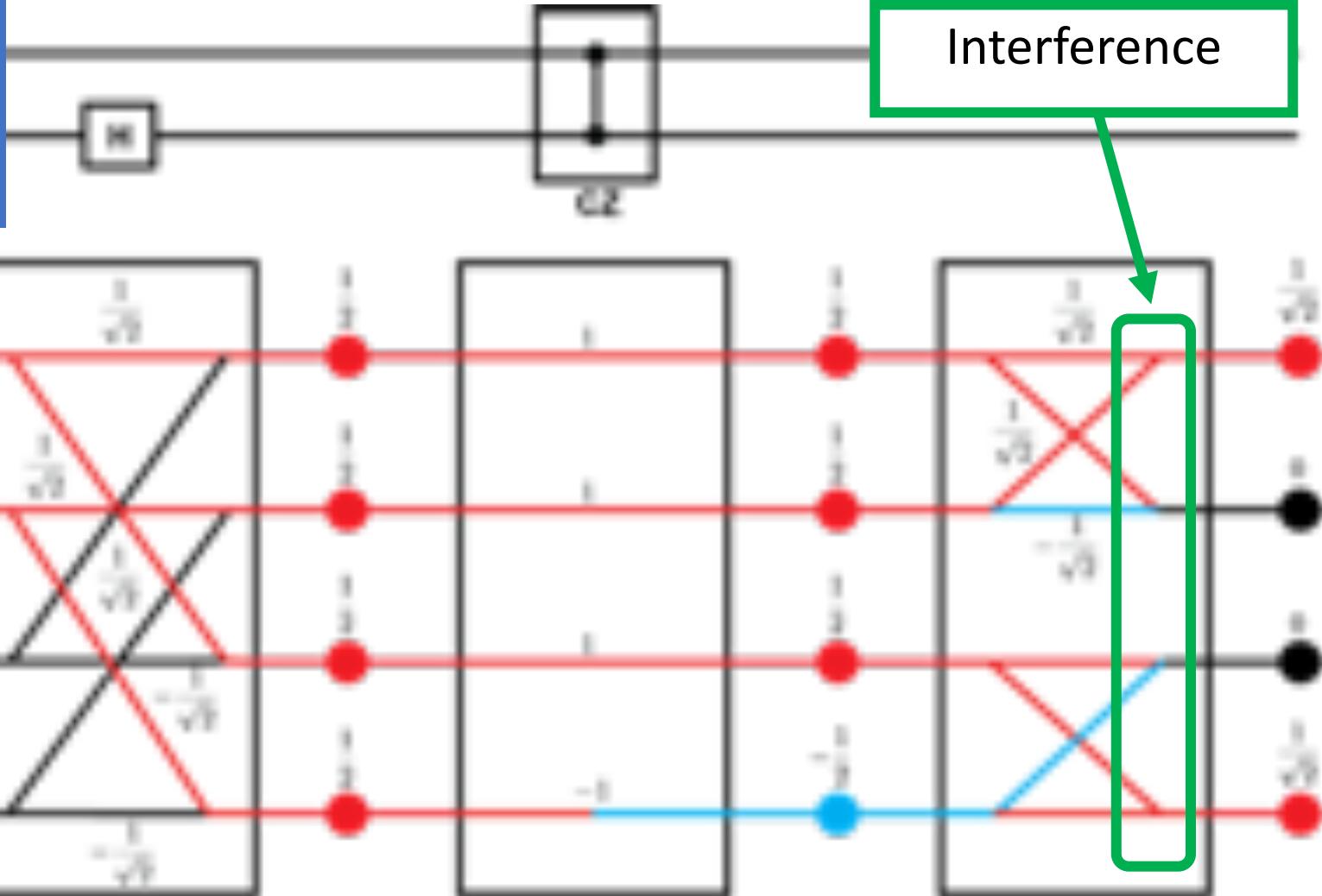
ting



Key Ingredients

- Unitary gate operations
- Superposition
- Entanglement
- Interference
- Projective measurement

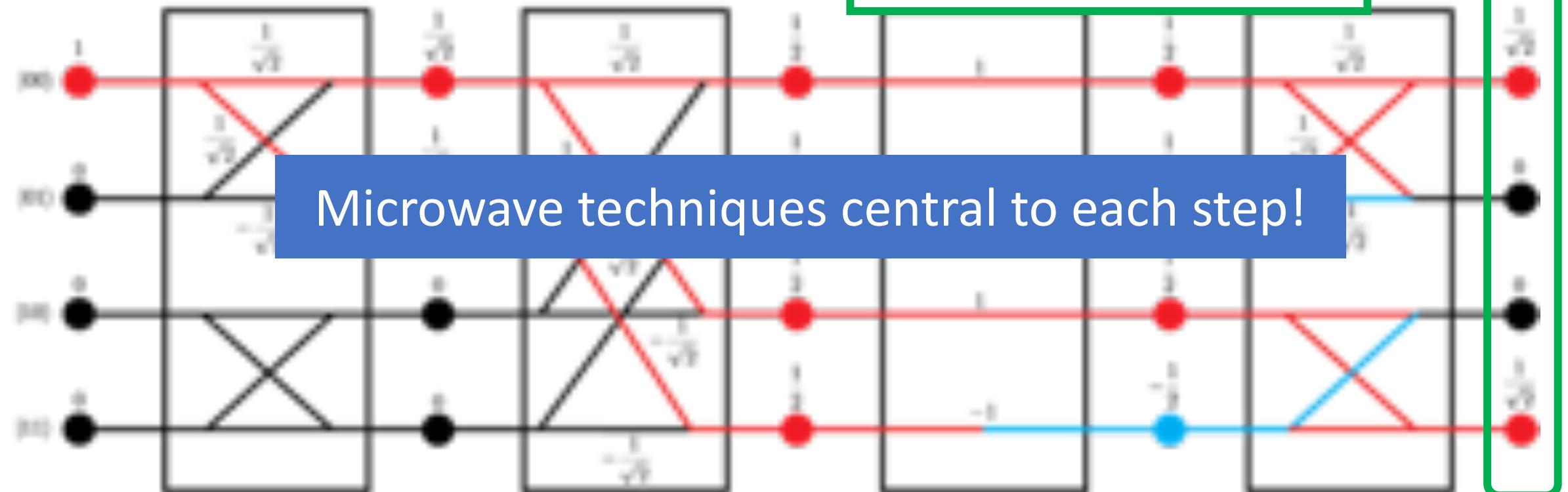
ting



Key Ingredients

- Unitary gate operations
- Superposition
- Entanglement
- Interference
- Projective measurement

ting



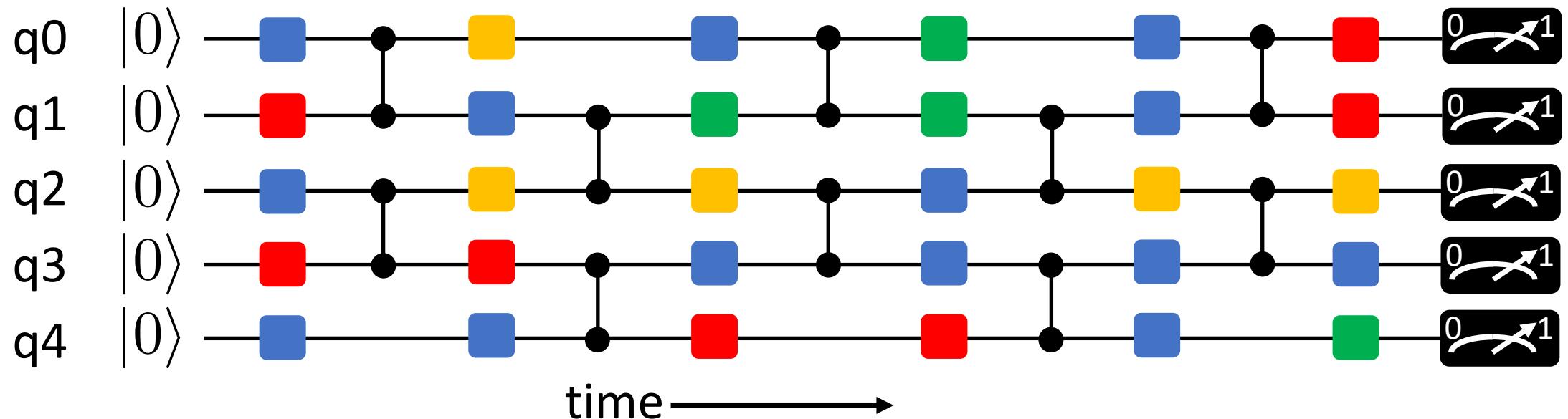
Projective measurement
(if both qubits measured)

$$P_{|00\rangle} = 0.5 \quad P_{|01\rangle} = 0 \\ P_{|10\rangle} = 0 \quad P_{|11\rangle} = 0.5$$

Universal Quantum Computing

Transformed state ($2^N \times 1$) $\rightarrow |\psi'\rangle = \hat{U}|\psi\rangle$ ← Initial state ($2^N \times 1$)
Unitary operation ($2^N \times 2^N$)

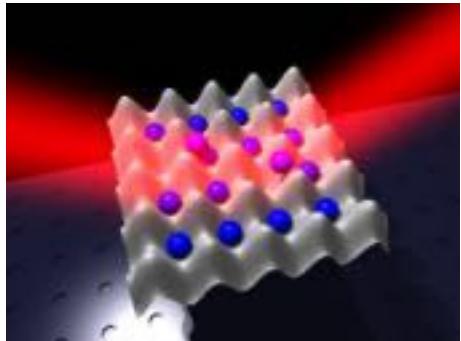
Ingredients:



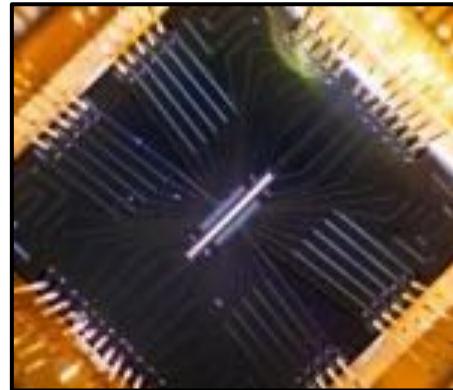
Building a quantum computer

Many Possible Qubit Technologies

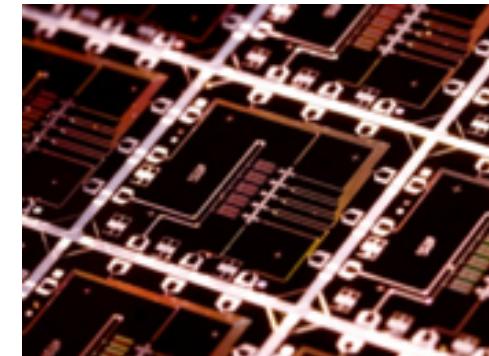
Atoms



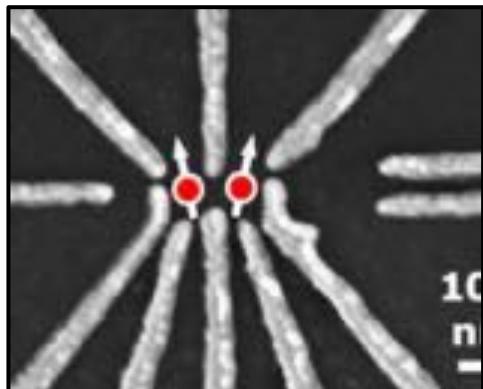
Ions



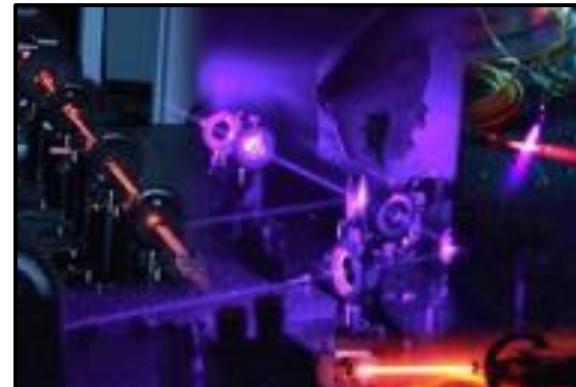
Superconducting
circuits



Spins

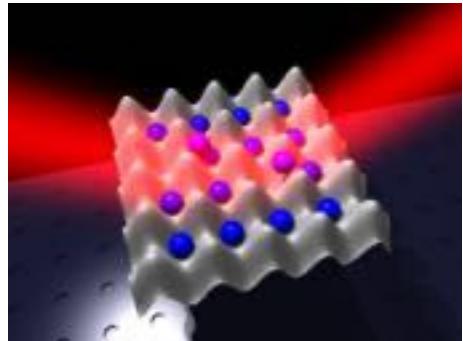


Optics

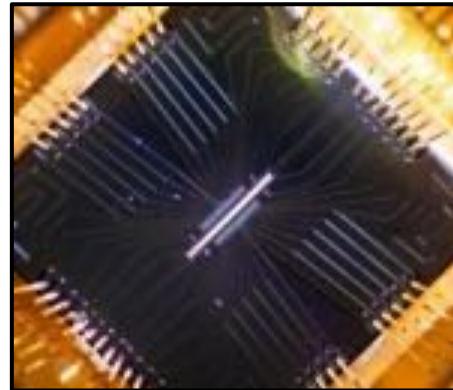


Many Possible Qubit Technologies

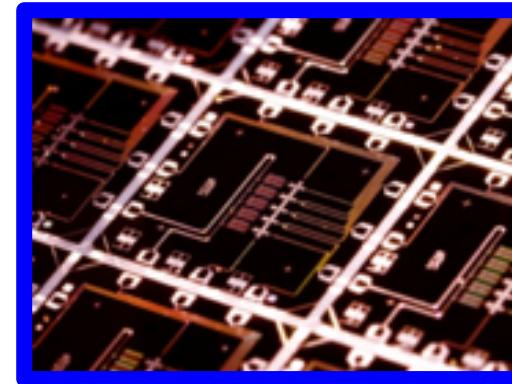
Atoms



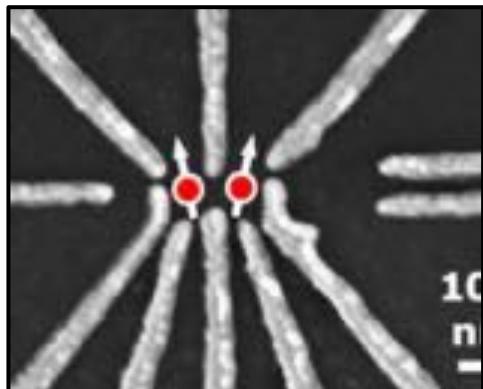
Ions



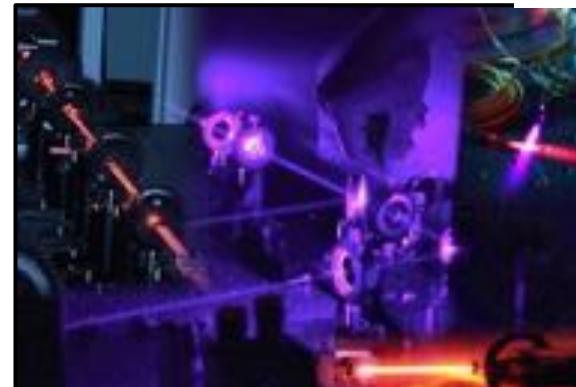
Superconducting
circuits



Spins

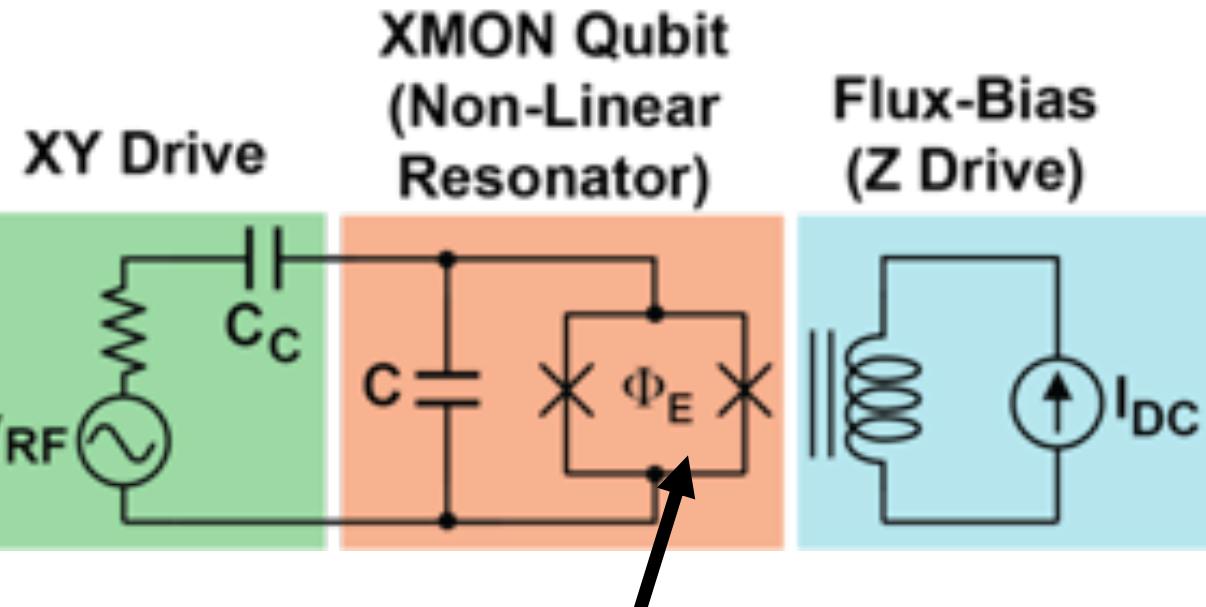


Optics



Google

Transmon Superconducting Qubit

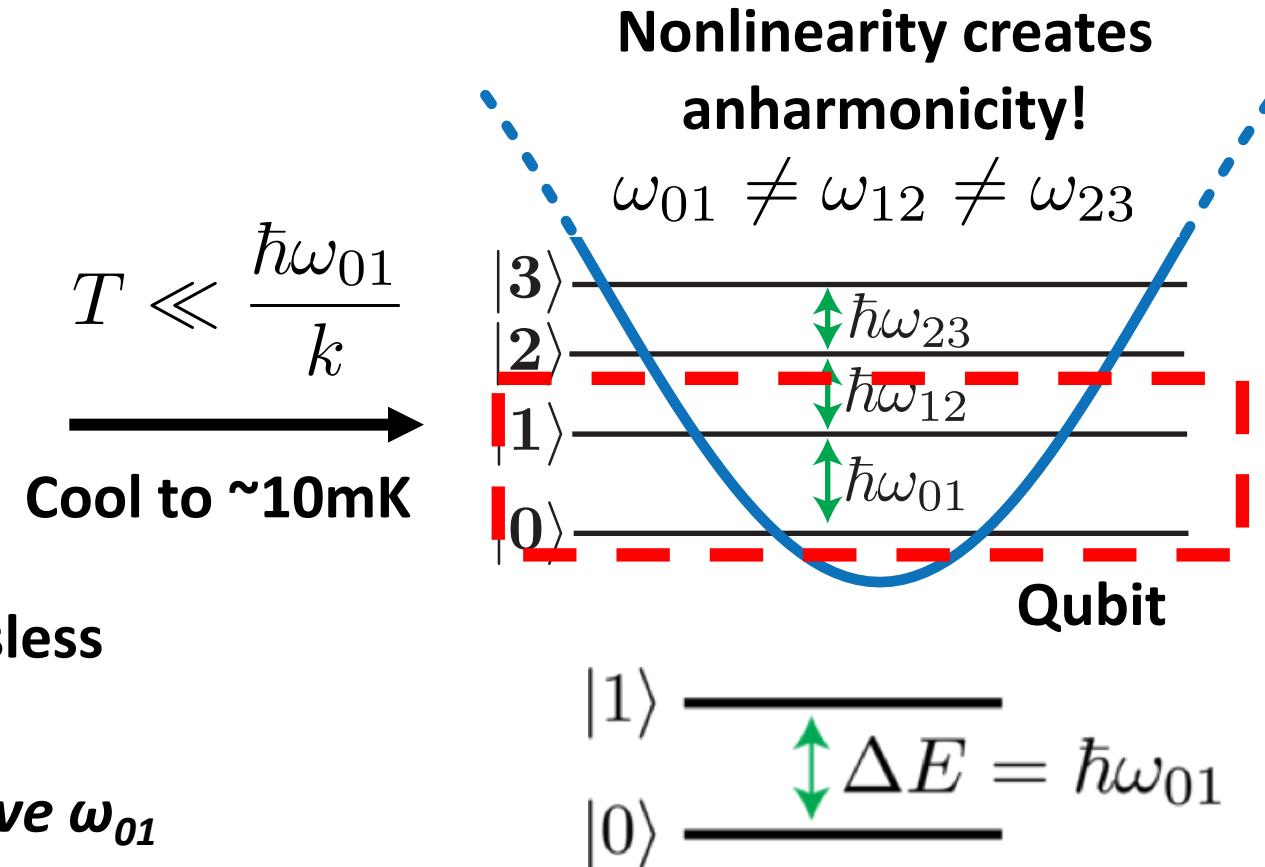


Josephson Junction loop \Rightarrow flux-tunable lossless nonlinear inductor

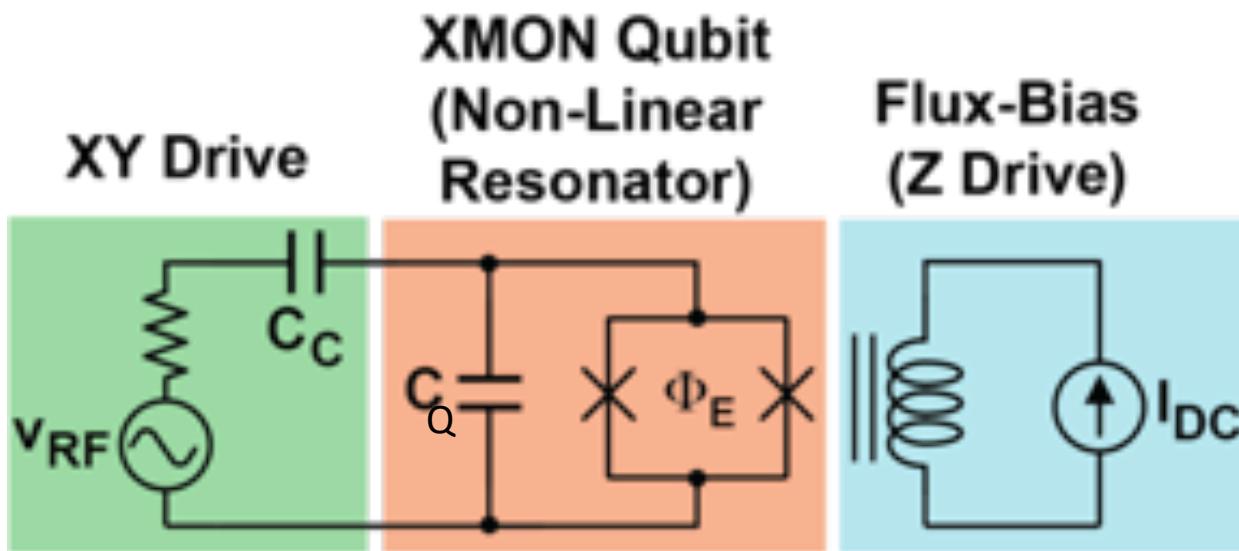
Behaves as 2-level qubit if we selectively drive ω_{01}

$$\omega_{01} \approx \frac{1}{\sqrt{L_{J0,e}(\Phi_E)C}} \Rightarrow 4\text{-}8\text{GHz}$$

$$\omega_{01} - \omega_{12} = \frac{q^2}{2\hbar C} \Rightarrow 0.15\text{-}0.35\text{GHz}$$



Transmon Superconducting Qubit



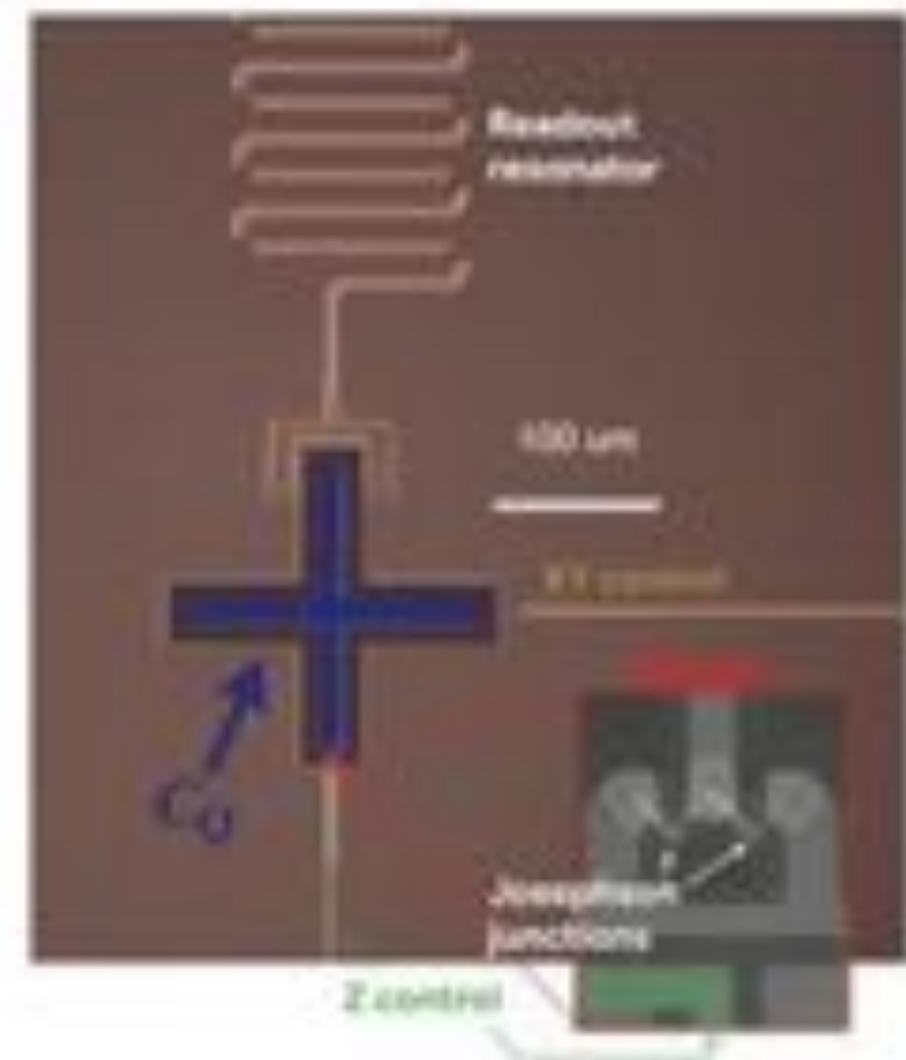
Typical component values

C_Q : 80fF

L : 8nH

C_C : 30aF

M : 2pH

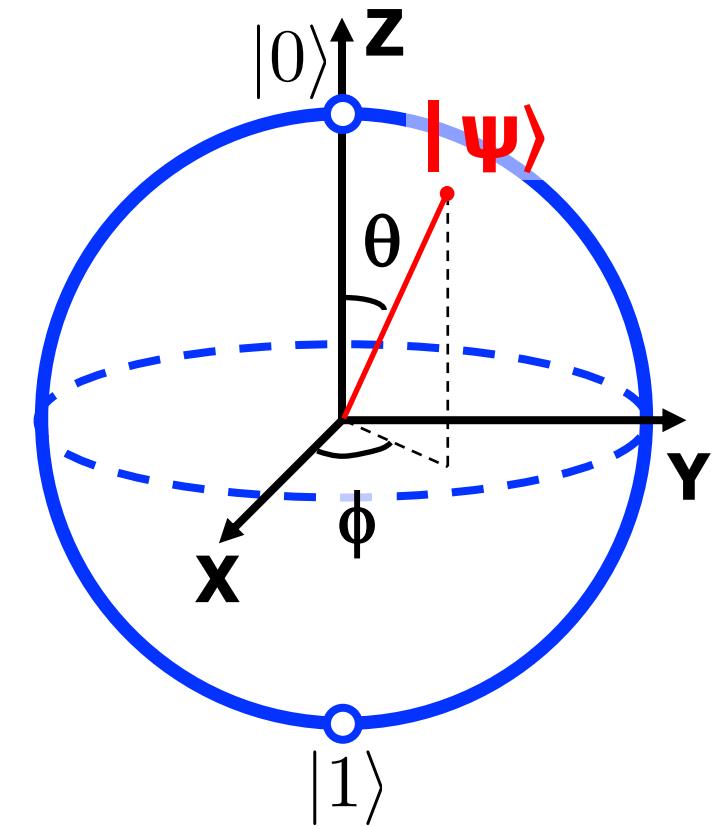


Bloch Sphere and Single Qubit Gates

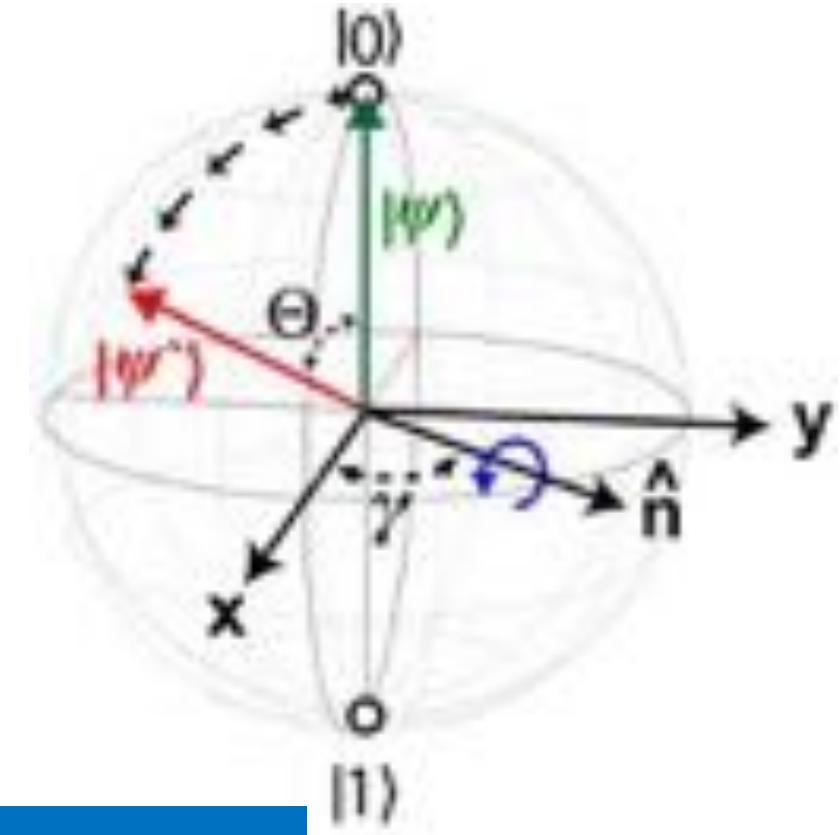
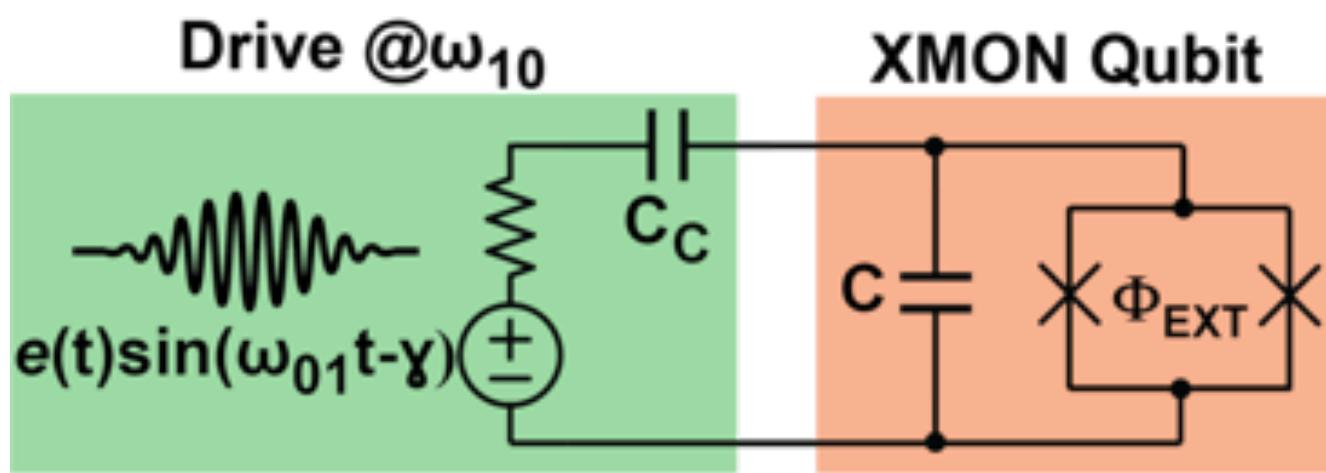
- During computation: can be in **superposition state (vector)**

$$|\psi\rangle = \cos\left(\frac{\theta}{2}\right) |0\rangle + \exp\{j\phi\} \sin\left(\frac{\theta}{2}\right) |1\rangle$$

- **Single qubit gate operations** correspond to **rotations** of Bloch vector.
- **Universal single-qubit gateset requires rotations about X, Y, & Z axes**



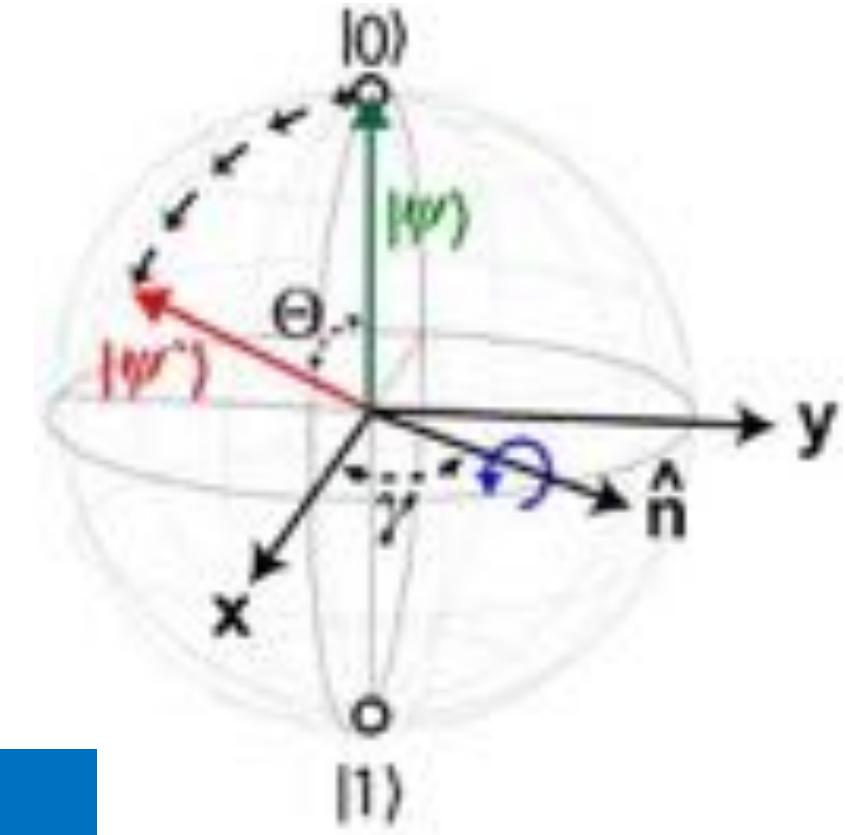
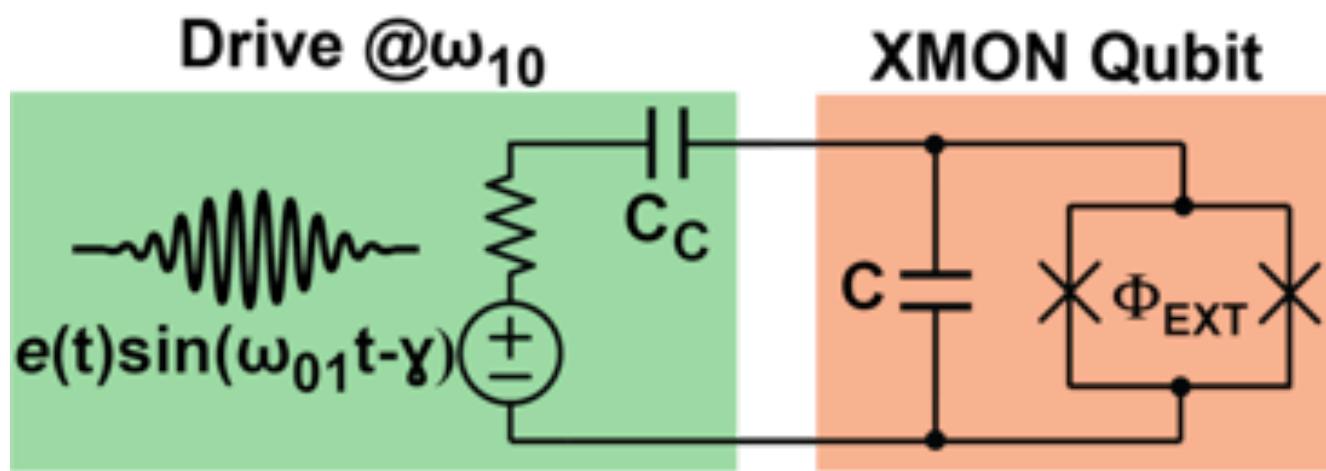
Single Qubit Gates on Transmon: XY Rotations



$$\Theta = \frac{\alpha}{\hbar} \int_{t_0}^t e(t') dt'$$

XY Gates: resonant microwave drive
Angle of rotation: integrated envelope amplitude
Axis of rotation: microwave carrier phase

Single Qubit Gates on Transmon: XY Rotations

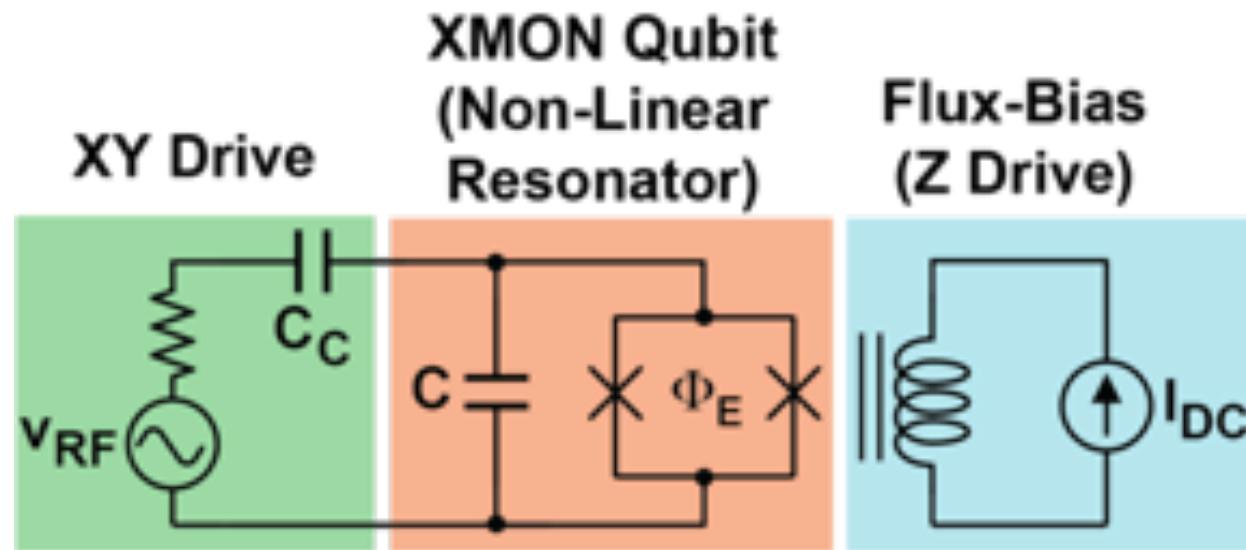


$$\Theta = \frac{\alpha}{\hbar} \int_{t_0}^t e(t') dt'$$

Typical values

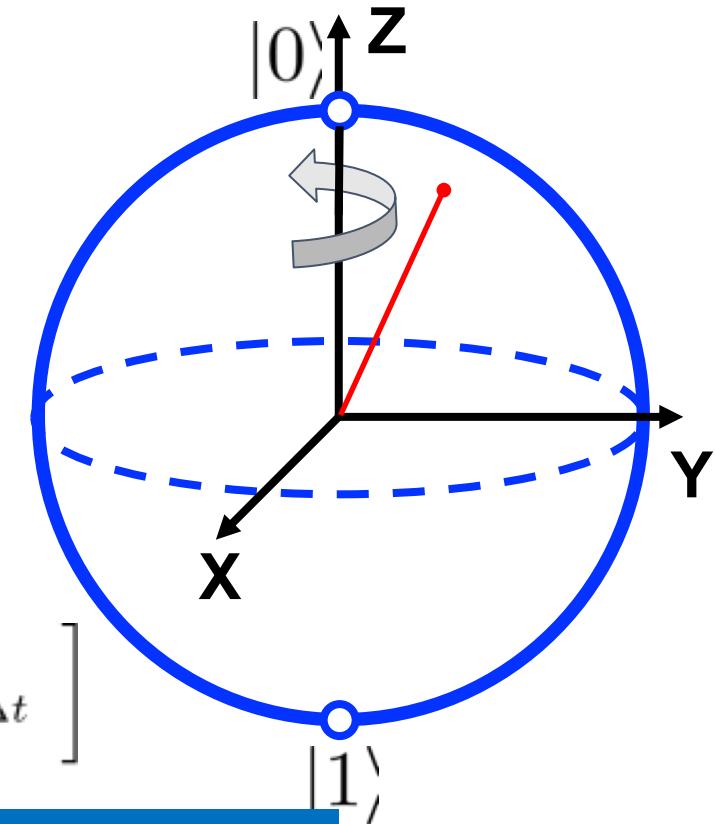
- Duration: 10-30 ns
- Peak power: -70 dBm
- Noise floor: -204 dBm/Hz

Single Qubit Gates on Transmon: Z Rotations



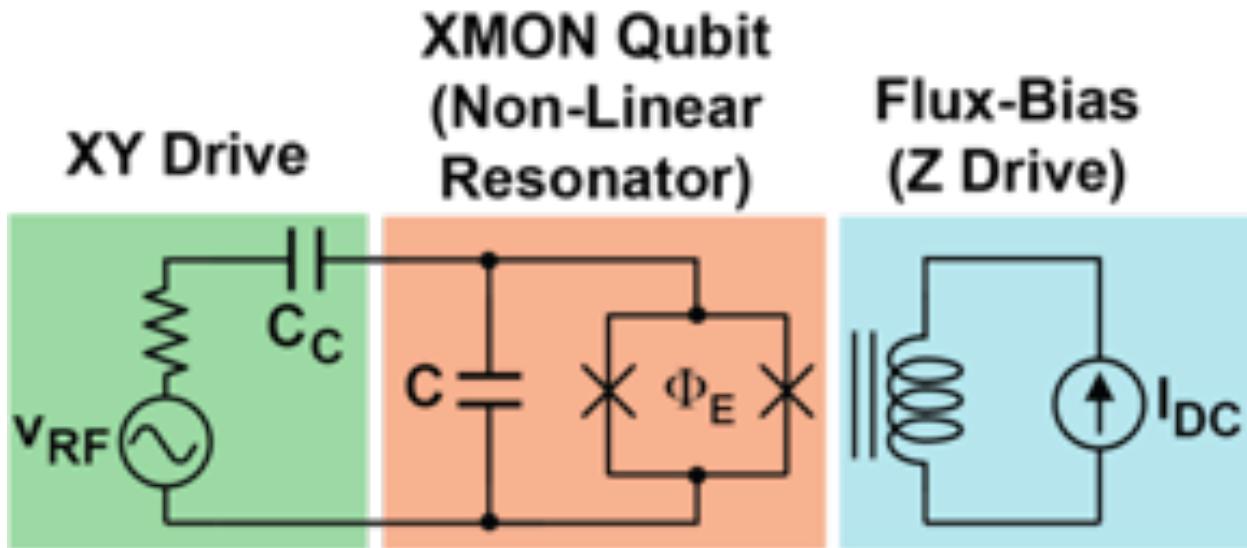
Use frequency control knob (flux bias current)

Detune by $\Delta\omega_{01}$ for duration Δt $\rightarrow \hat{U}_Z = \begin{bmatrix} 1 & 0 \\ 0 & e^{-j\Delta\omega_{01}\Delta t} \end{bmatrix}$



Z gate design: duration and amplitude of current pulse

Single Qubit Gates on Transmon: Z Rotations

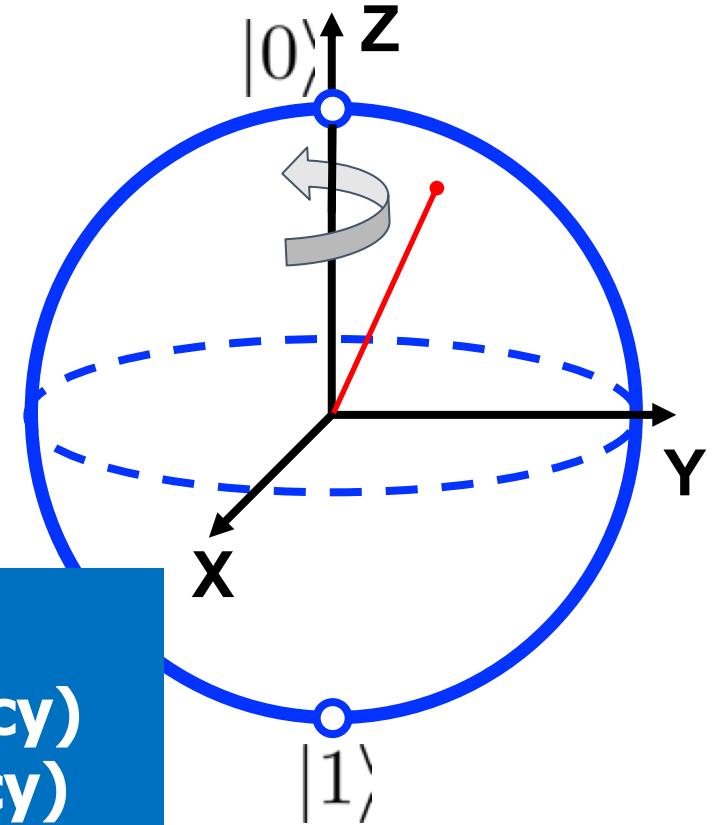


Use frequency control knob (flux bias current)

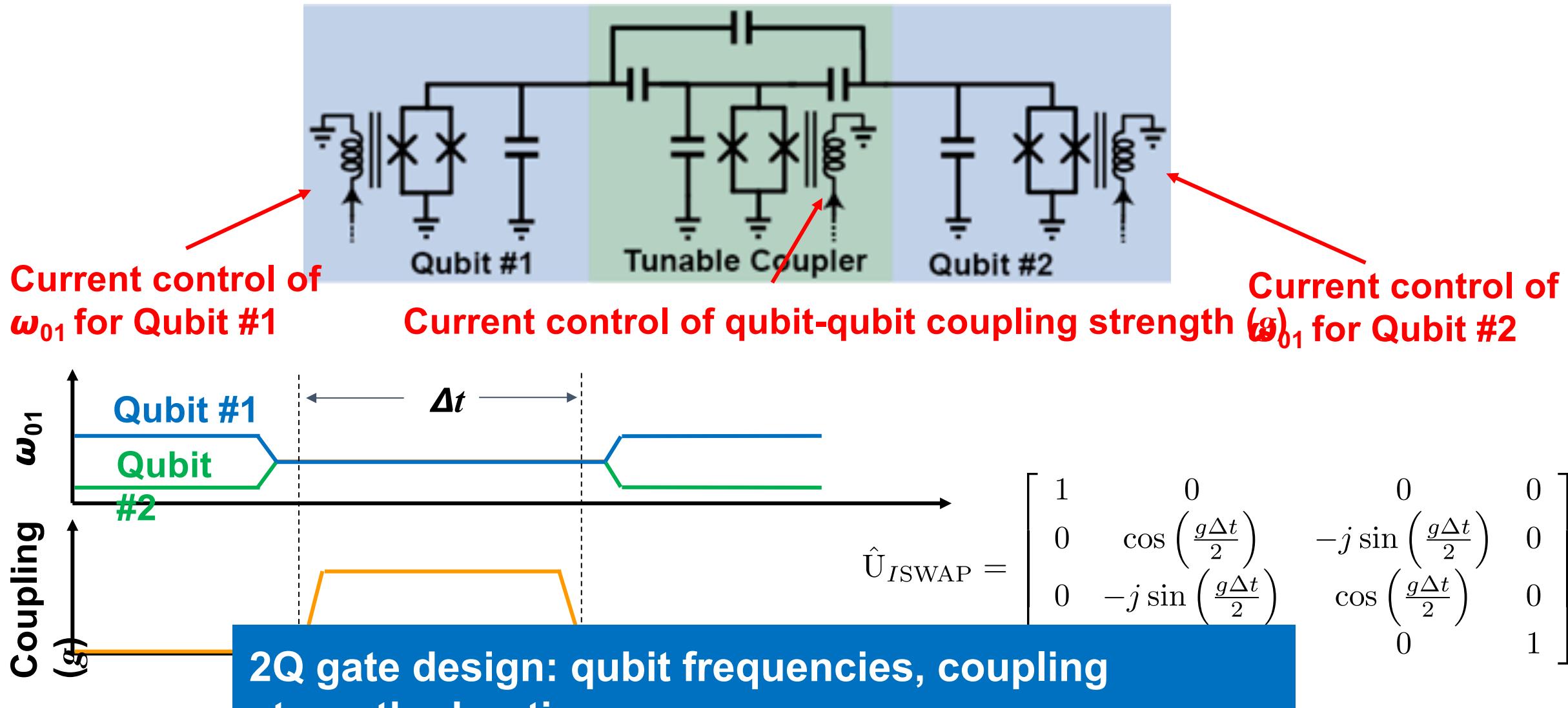
Detune by

- **Static Z-bias:** 50-500 μ A (14 bit accuracy)
- **Pulsed Z-bias:** 1-500uA (14 bit accuracy)
- **Pulse duration:** 5-50ns

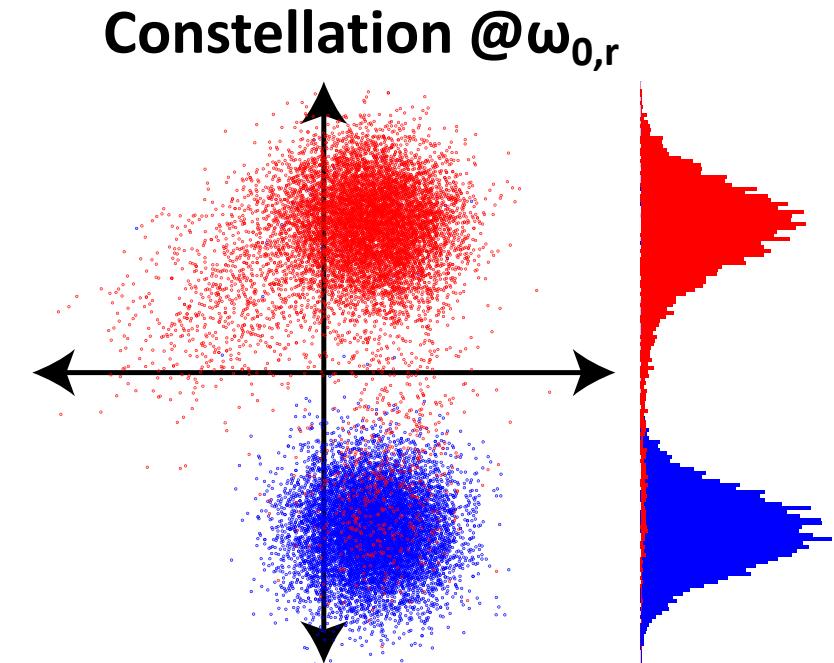
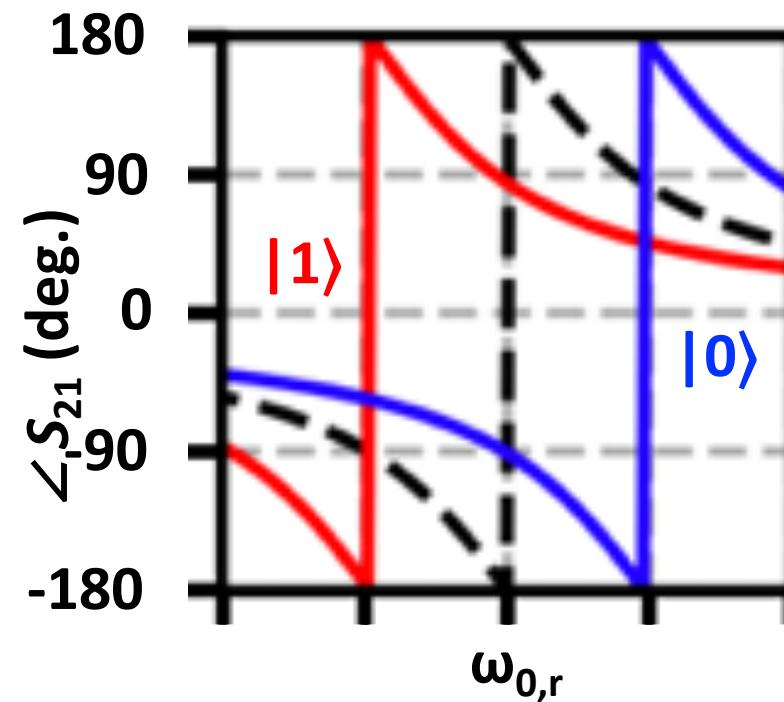
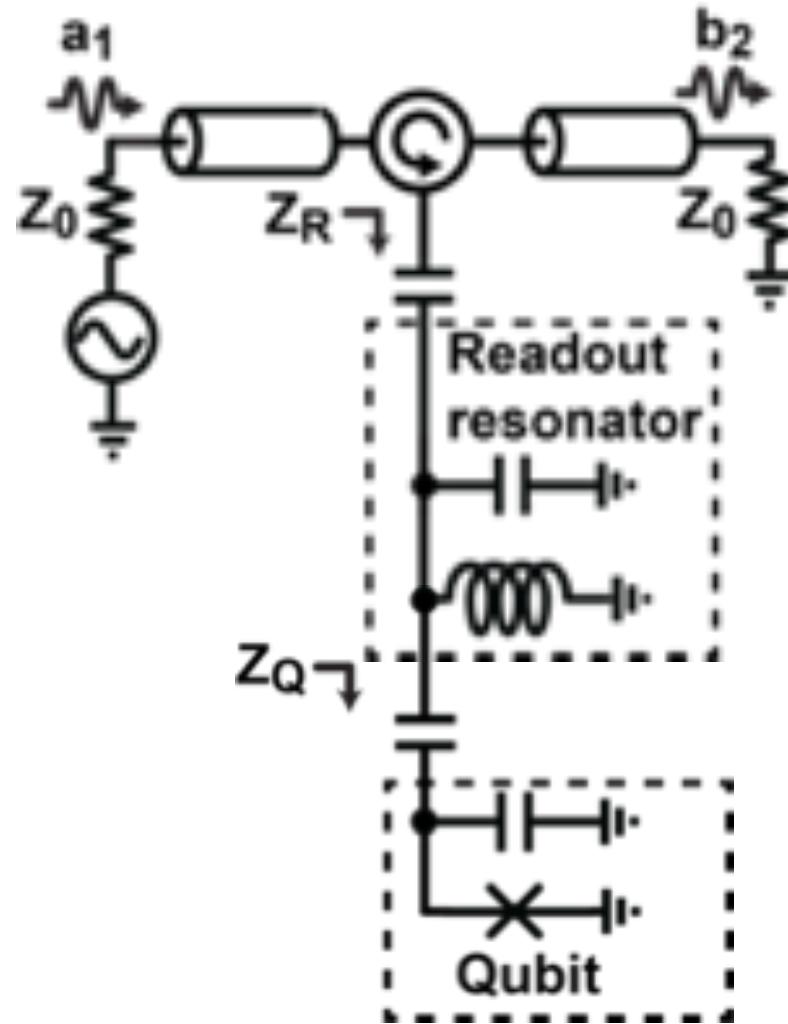
Typical values



2Q Gates Require Deterministic Interactions

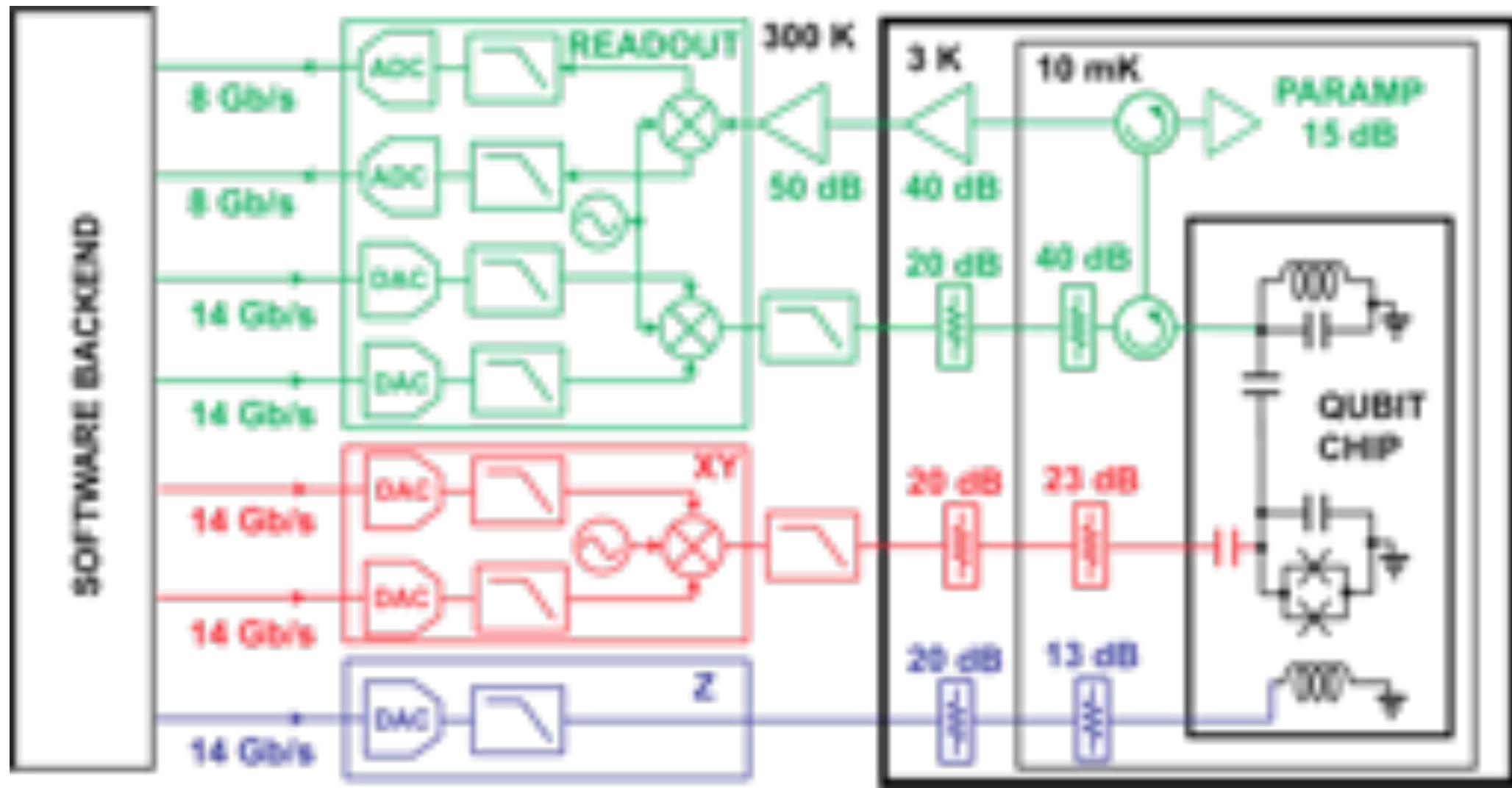


Dispersive Readout of Transmon



- Projective measurement: qubit assumes result
- State info encoded in phase (BPSK-like)
- One-shot meas. in 0.2-1us
- 5-10 qubits on one readout line
- Requires quantum limited amplification

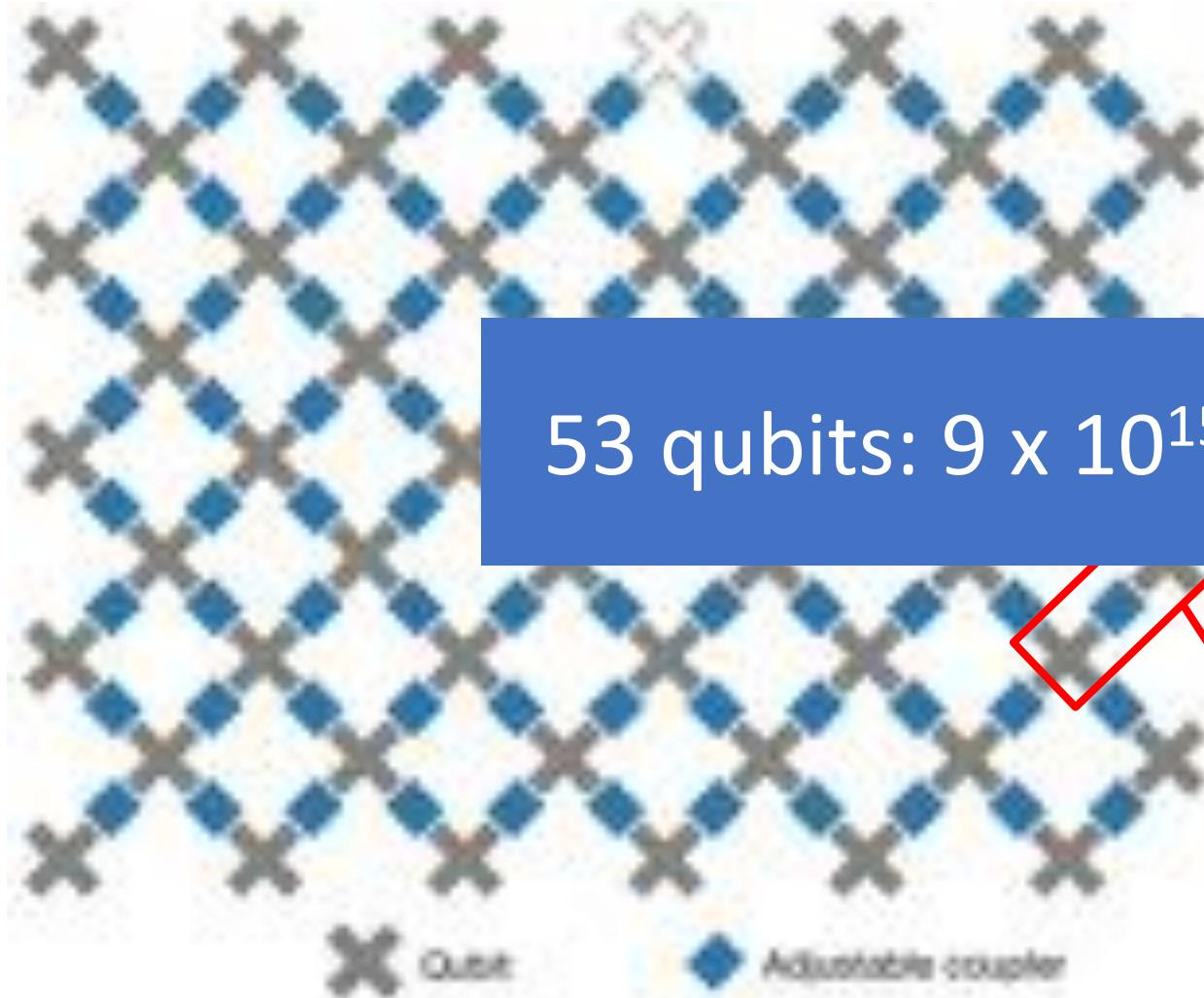
Control/Measurement for One Qubit



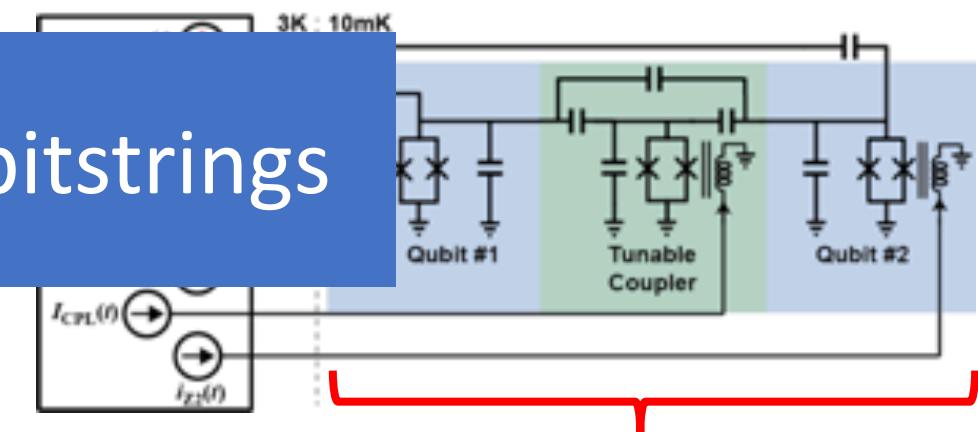
What can today's quantum computers do?



Sycamore Processor (53 qubits)

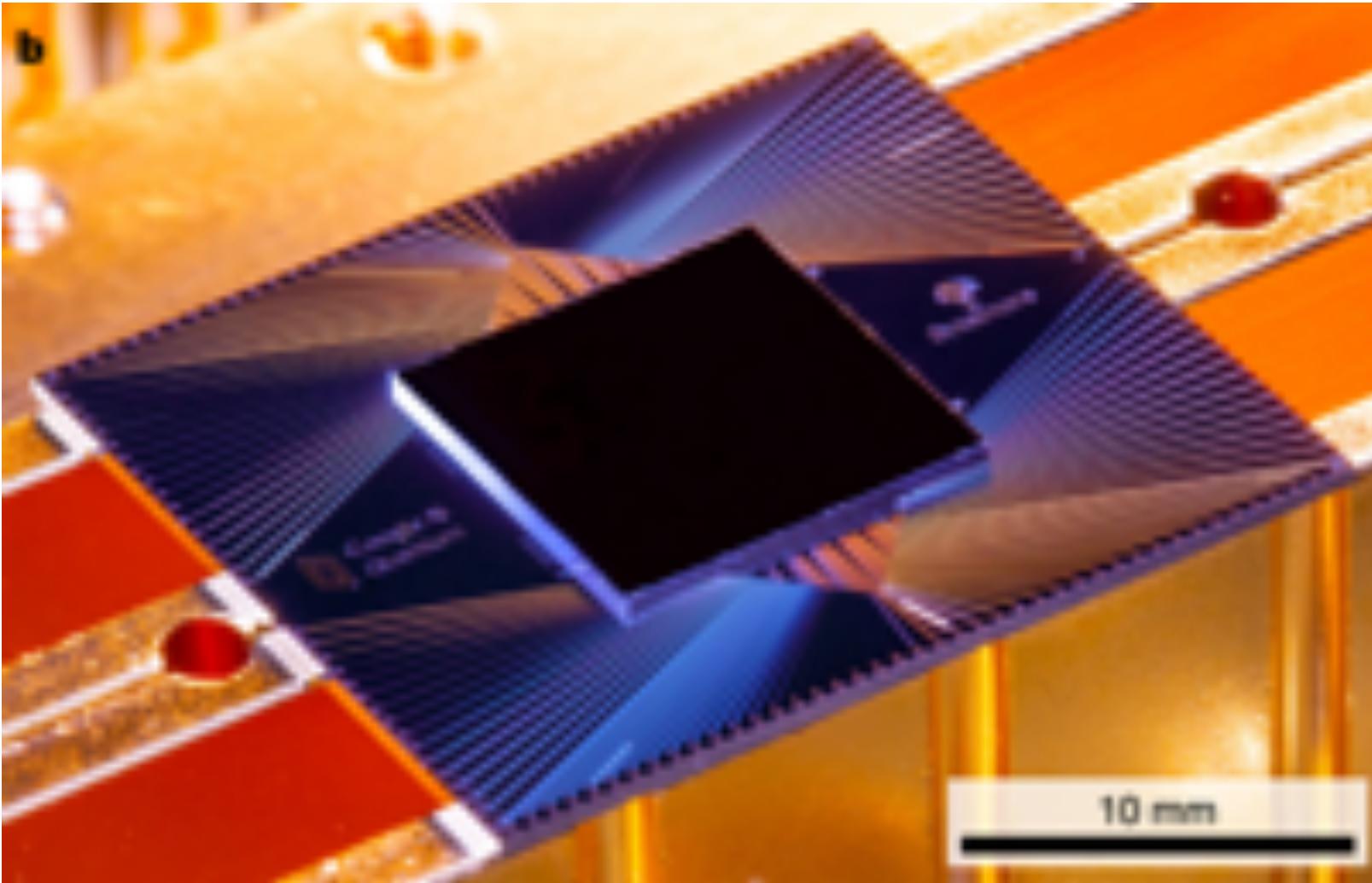


53 qubits: 9×10^{15} bitstrings

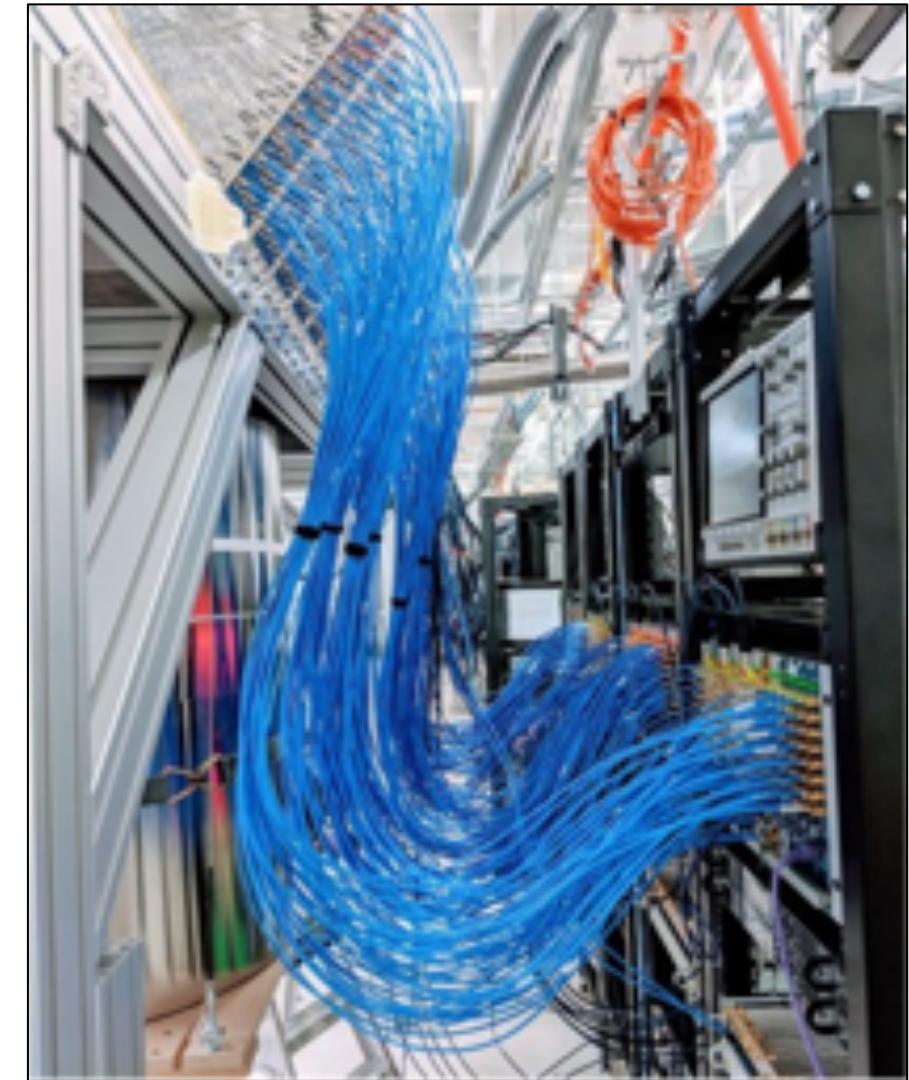
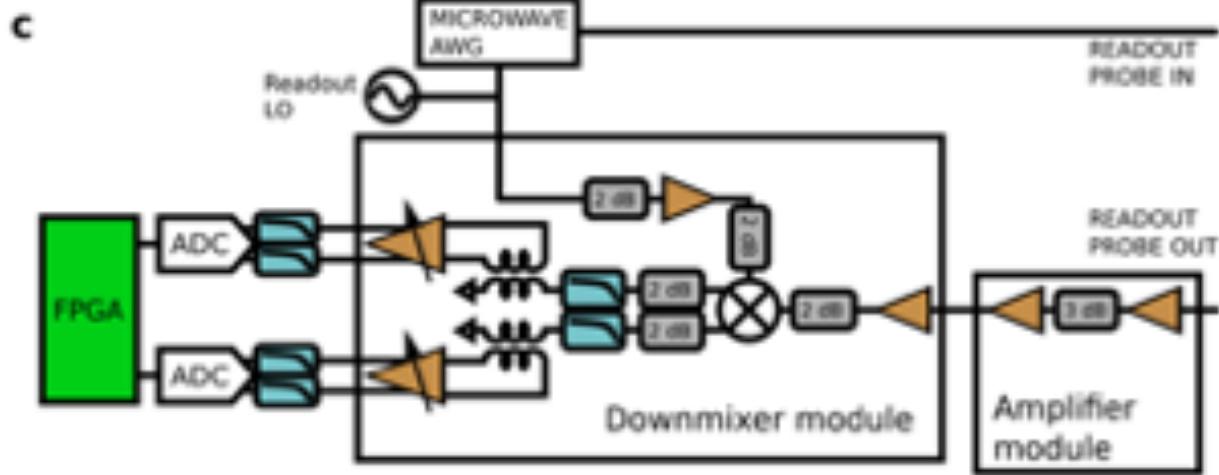
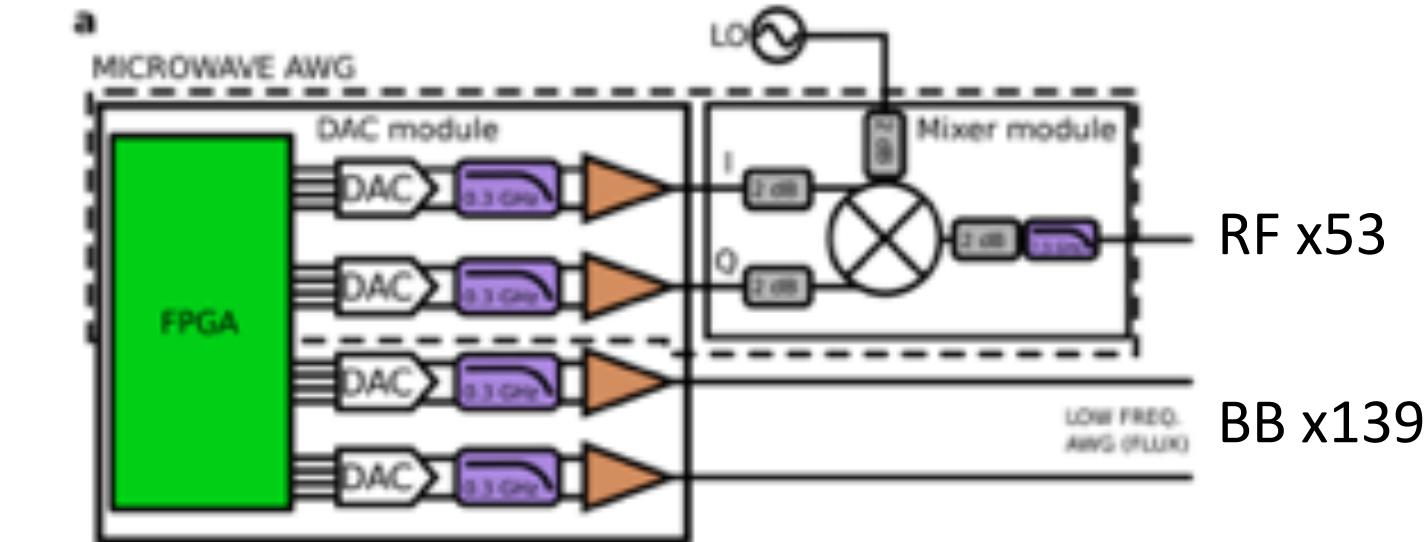




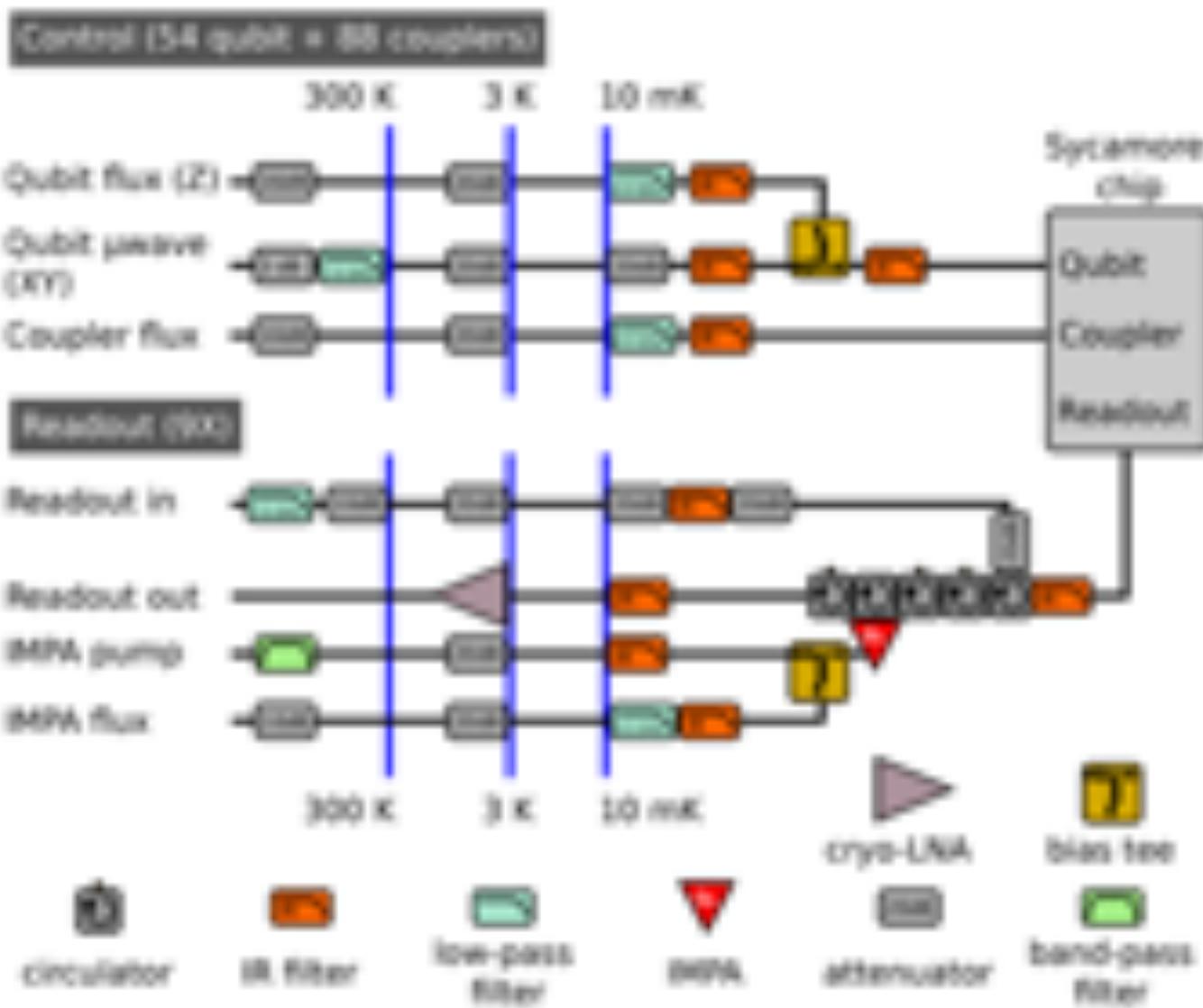
Sycamore Processor



Room Temperature Microwave Electronics

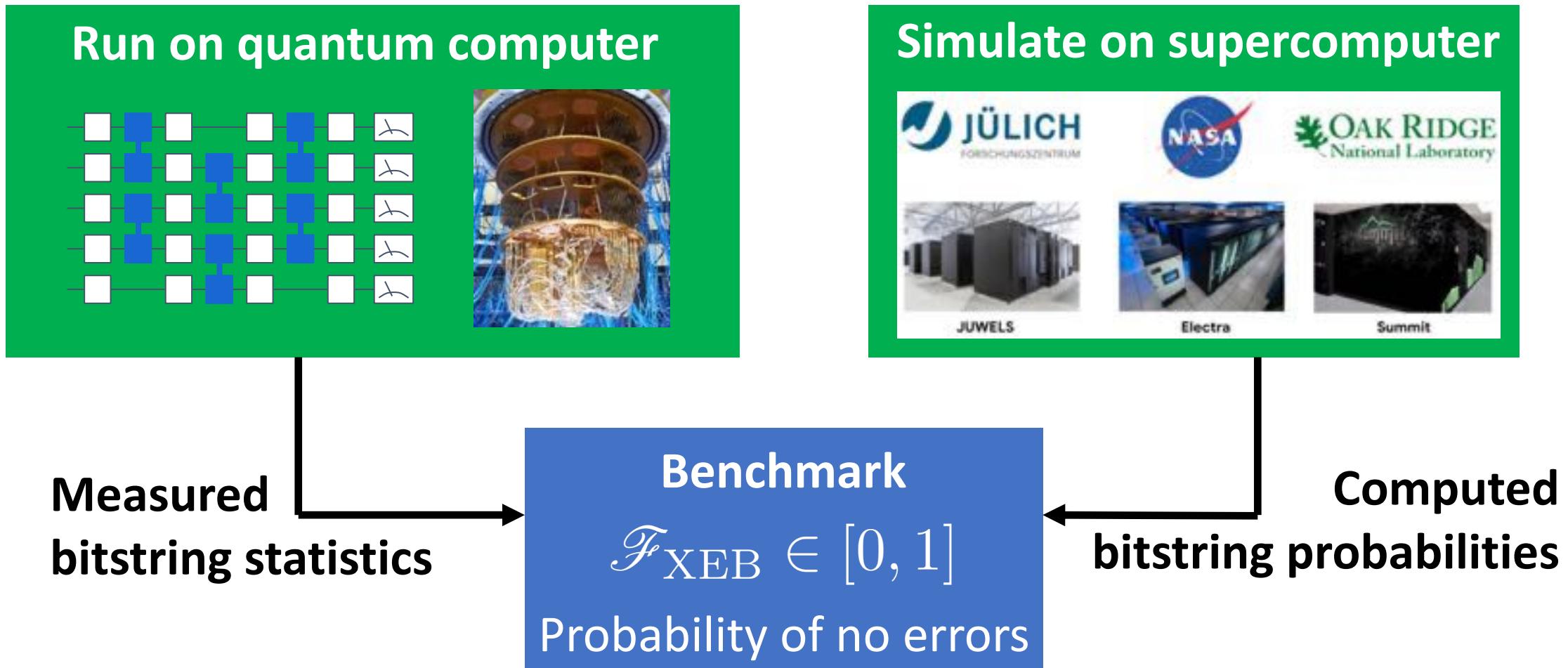


Cryogenic Microwave Electronics

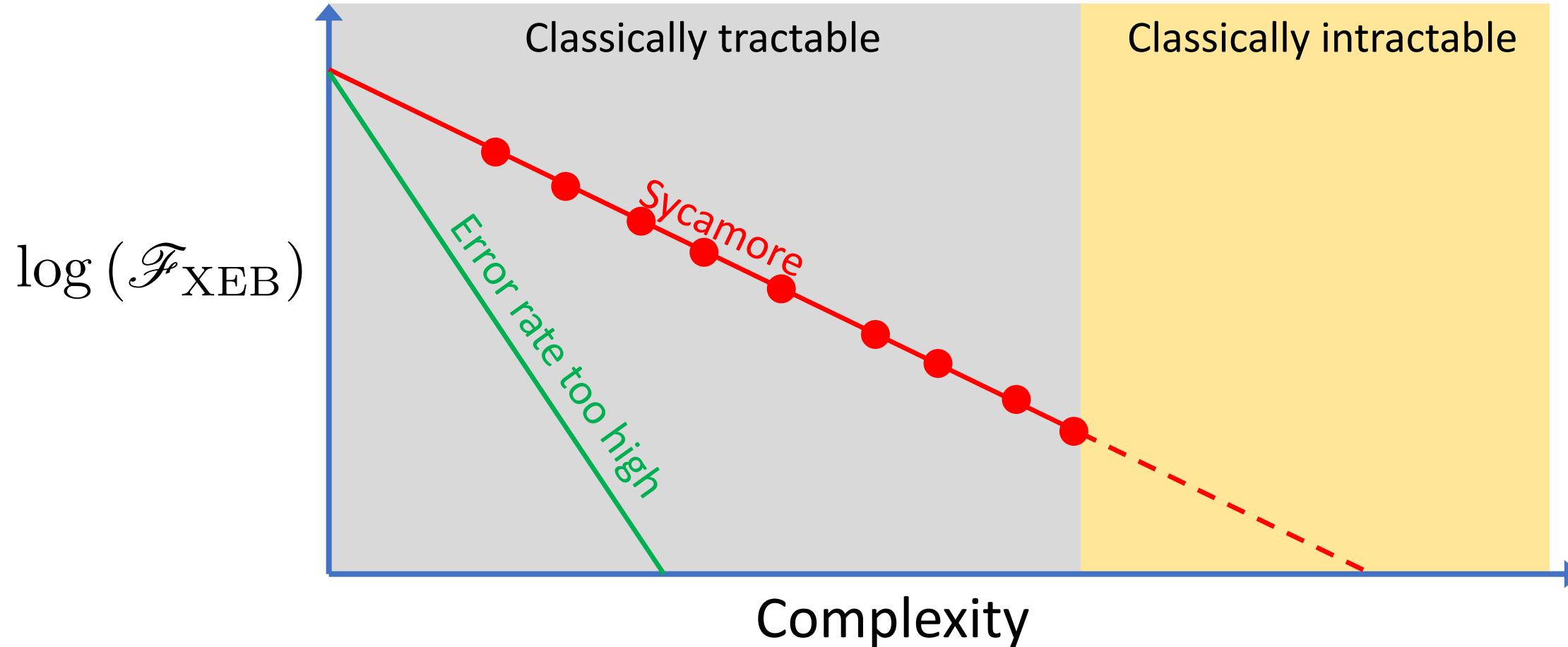


Recipe to demonstrate beyond-classical performance

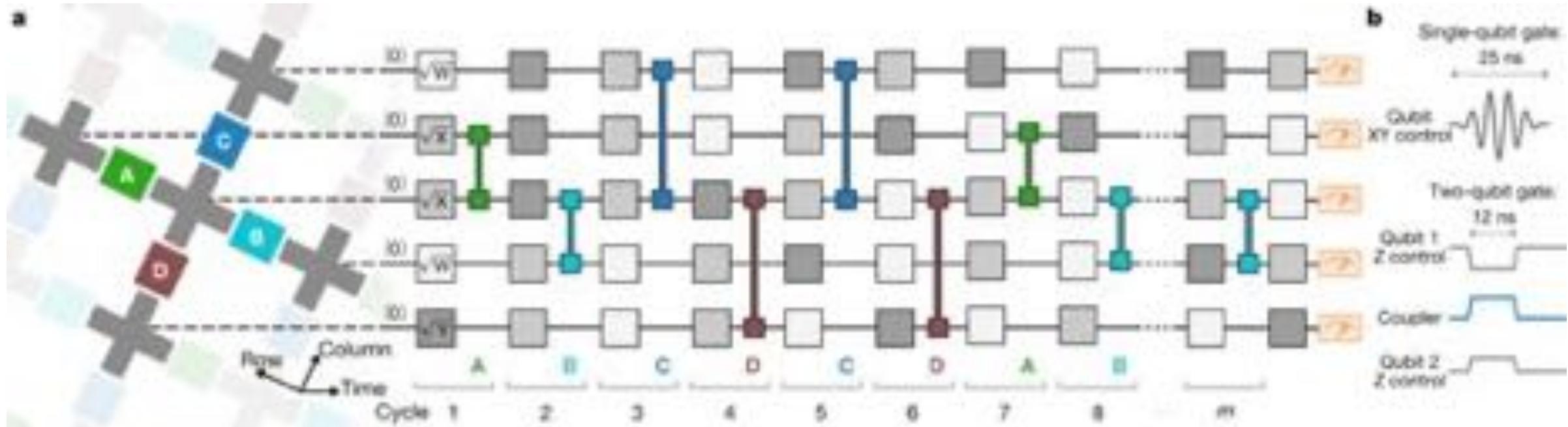
Algorithm: Sample bitstrings from 2^N Hilbert space (classically hard)



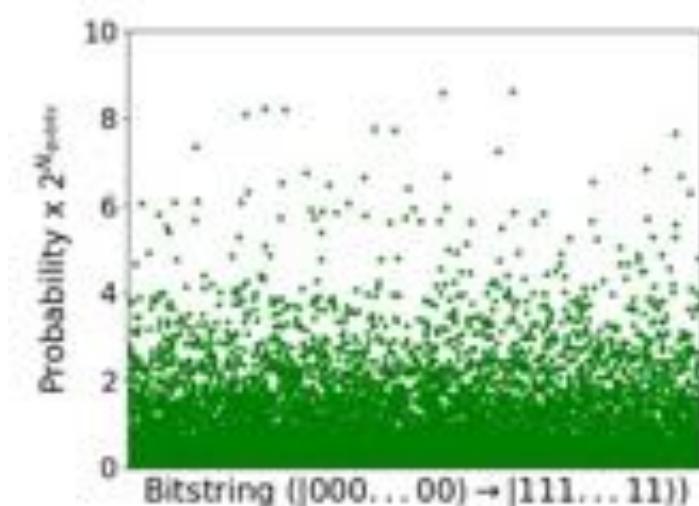
Recipe to demonstrate beyond-classical performance



Post-Classical Demonstration



- Randomly selected single qubit gates
- Two qubit gates in repeating pattern
- Statistics quickly approach Porter-Thomas exponential distribution



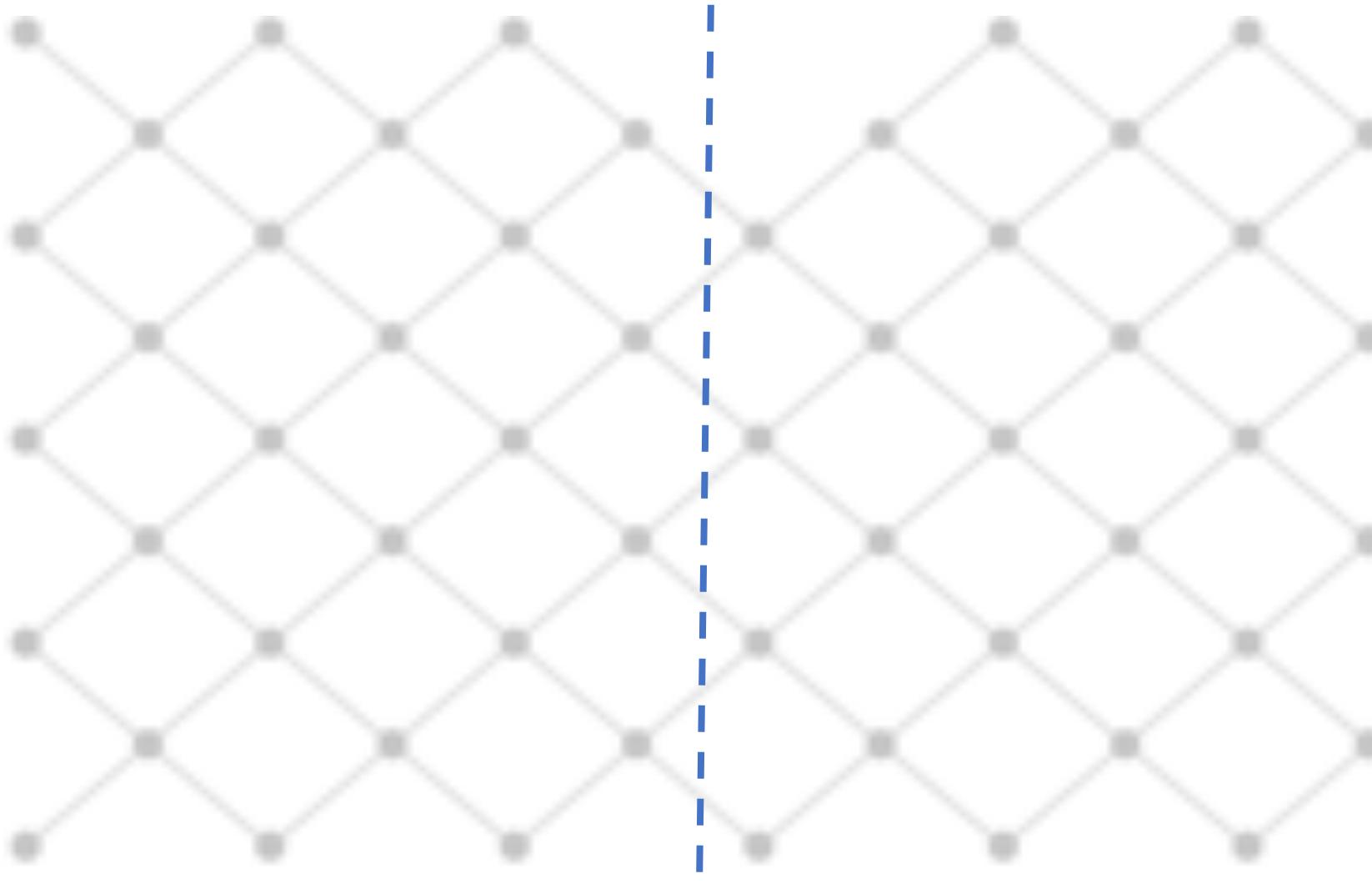
Post-Classical Demonstration

- Single qubit gate (random)
- Two qubit gate



Simplified ‘patch’ circuits

Single qubit gate (random)
Two qubit gate



Signal requirements for largest circuits

b

Single-qubit gate:

25 ns



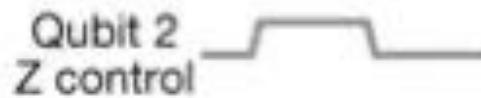
1113 single qubit gates = 1113 microwave pulses

Two-qubit gate:

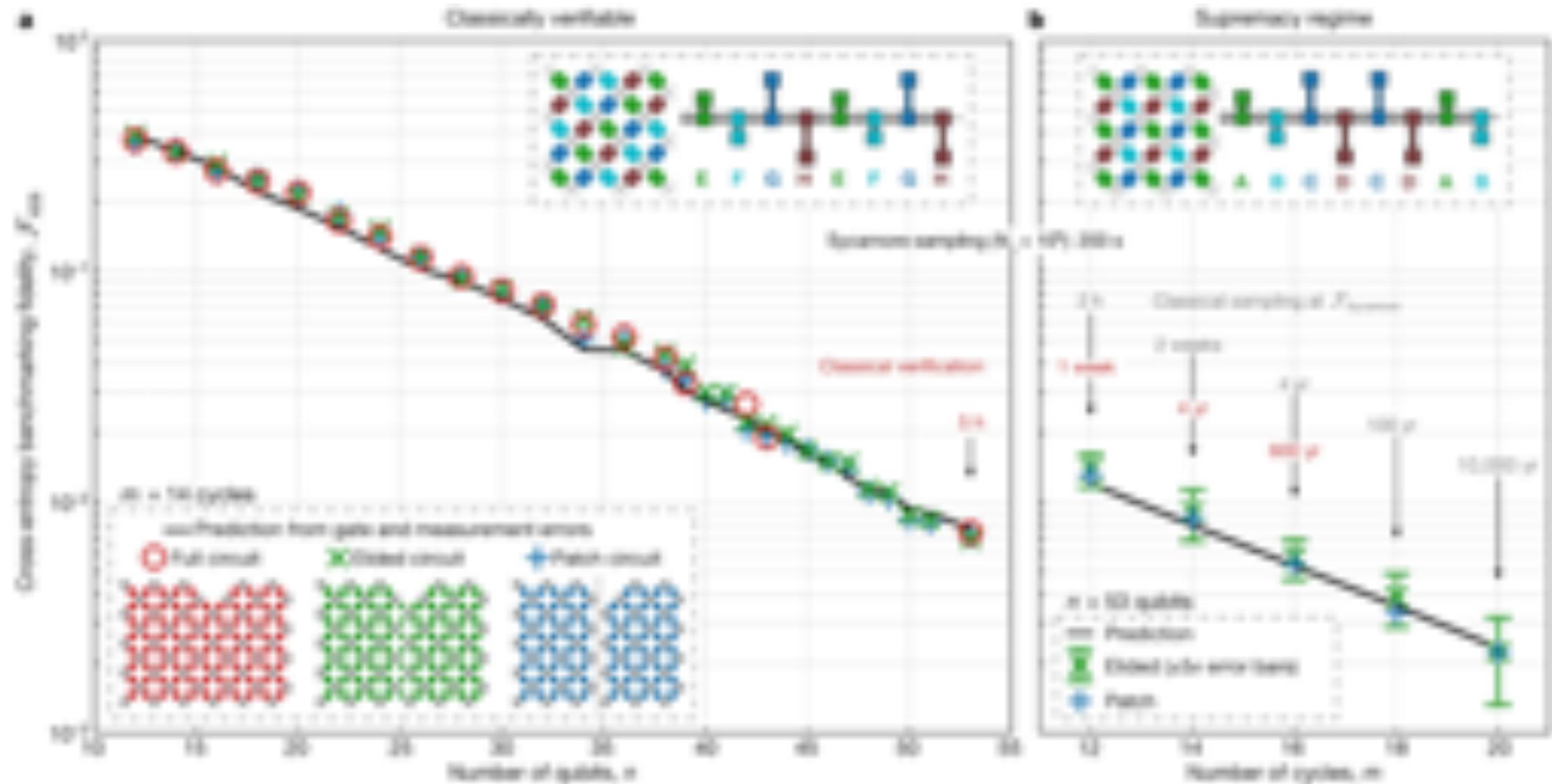
12 ns



430 two qubit gates = 1290 baseband pulses

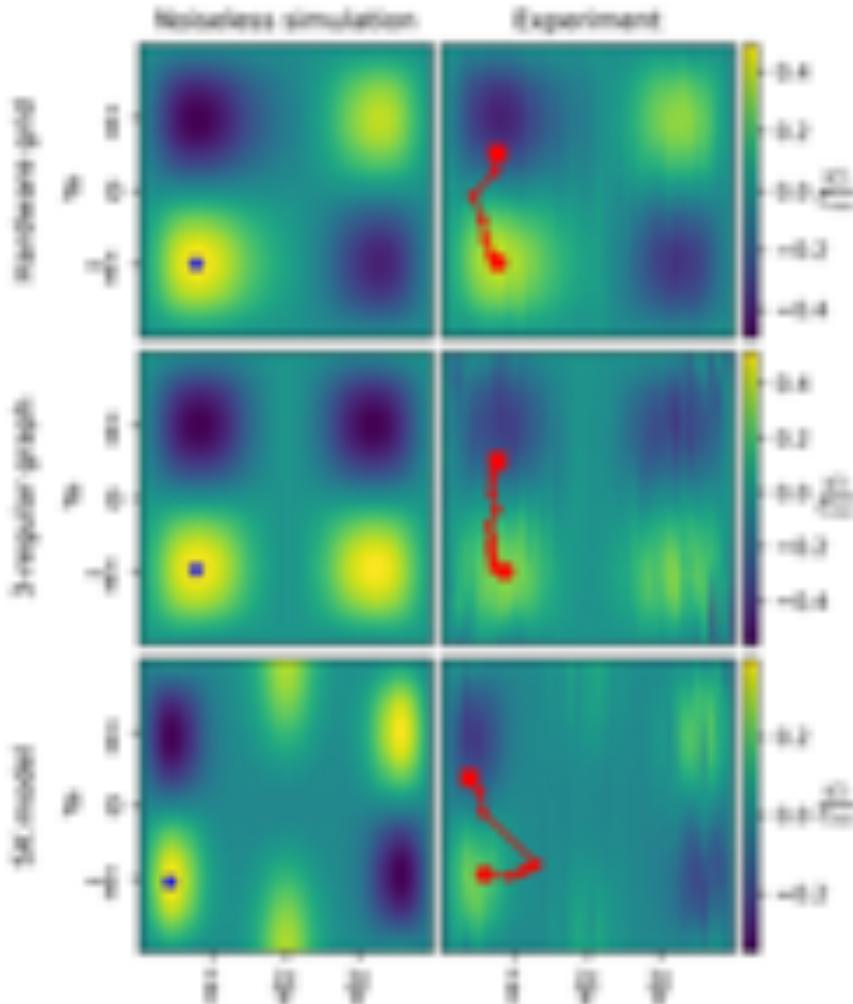


Experimental results



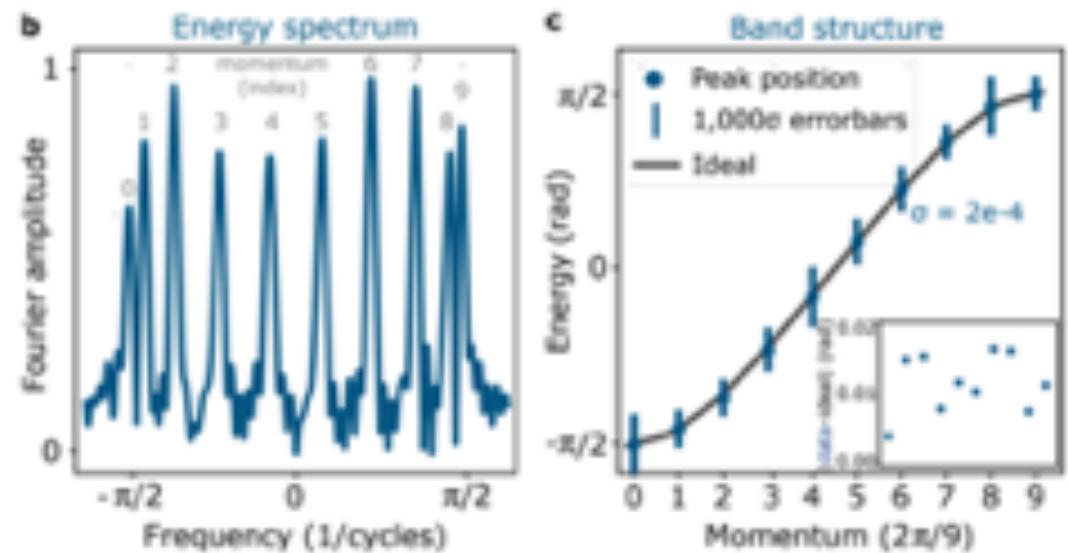
Applications of Sycamore

Optimization



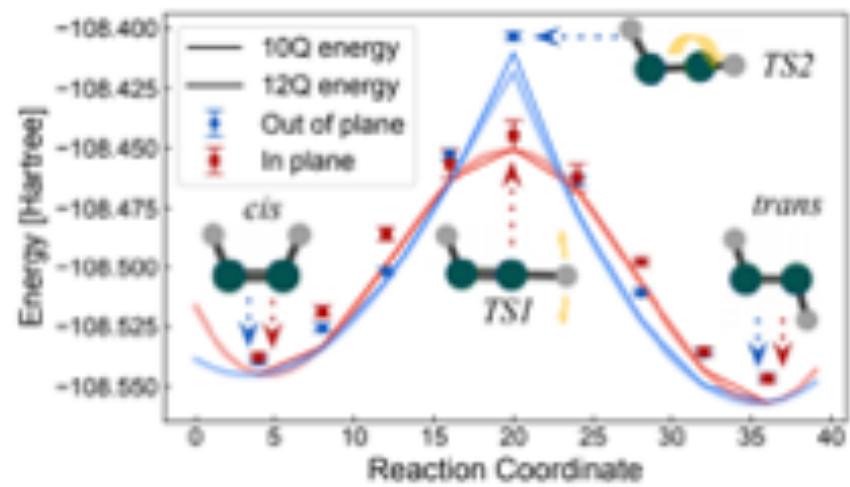
(Arute et al, Nature Physics, in press)

Band Structure



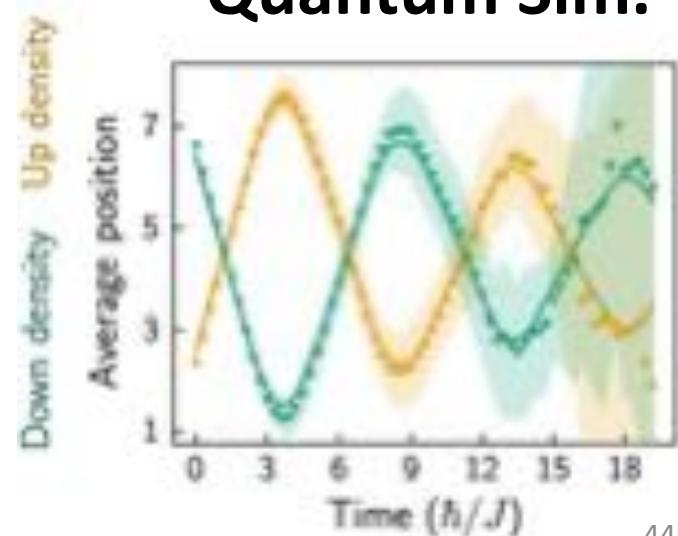
(Arute et al, ARXIV, 2020)

Chemistry



(Arute et al, Science, 2020)

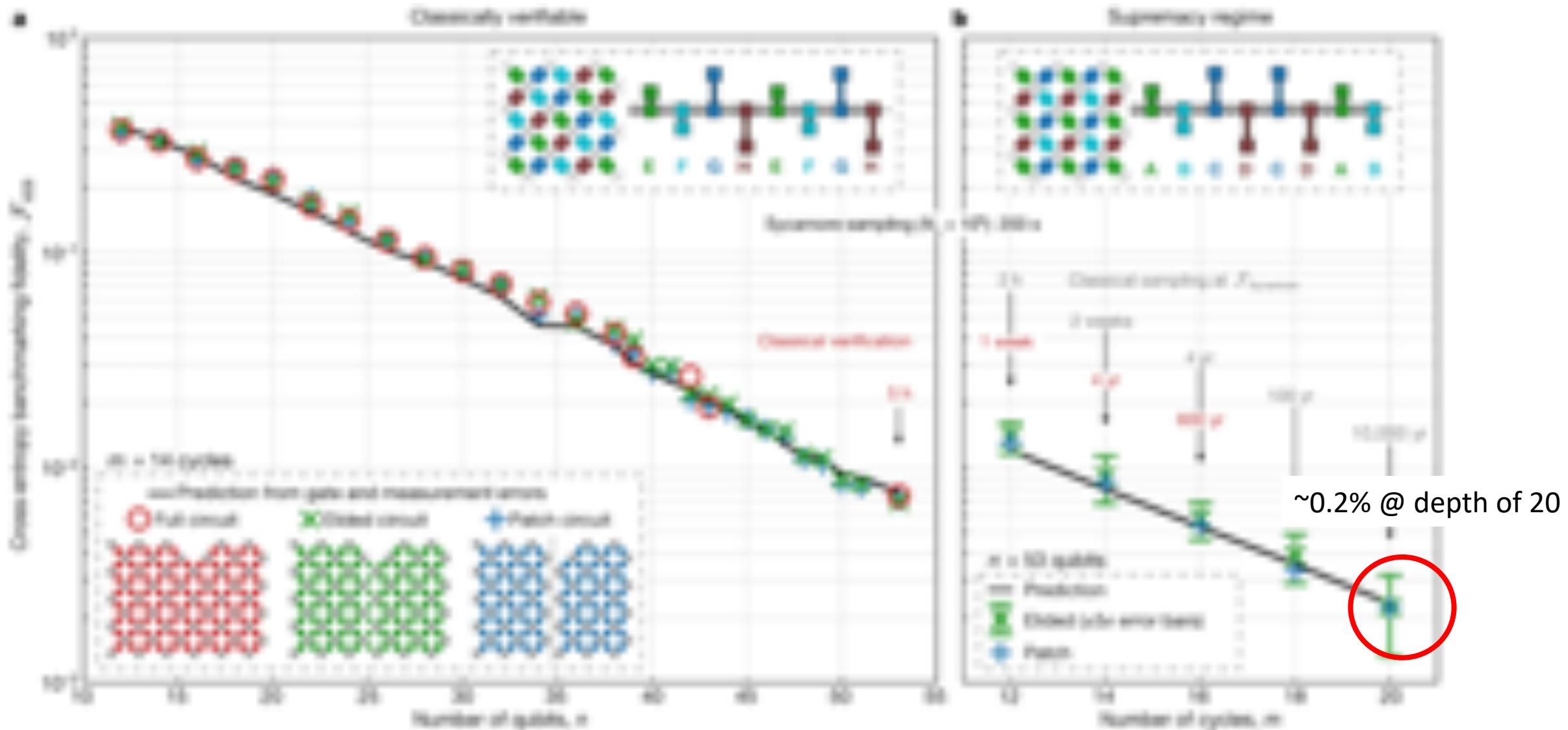
Quantum Sim.



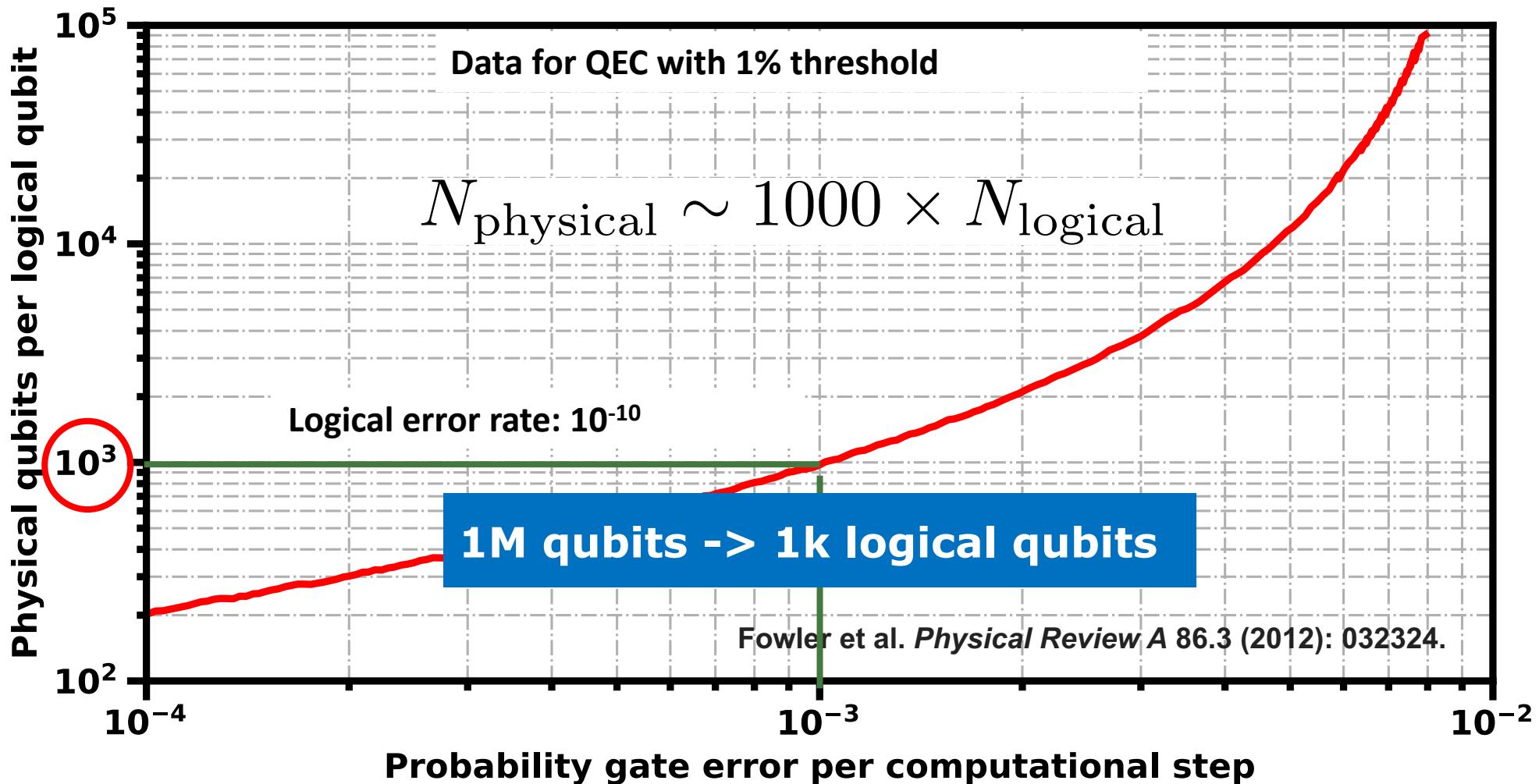
(Arute et al, ARXIV, 2020)

What microwave engineering is needed for the future?

53 qubits and depth of 20: probability of success $\sim 0.2\%$



Quantum error correction requires redundancy



1M Qubit Quantum Computers Targeted by Industry

MIT Tech Rev. 4/2020

**Inside the race to build
the best quantum
computer on Earth**

power of 1,000 qubits, you'd need a million actual ones. Google "conservatively" estimates it can build a million-qubit processor within 10 years, Neven says, though there are some big technical hurdles to overcome.

IBM promises 1000-qubit quantum computer—a milestone—by 2023

Science 9/15/2020

announced, she says. The plan includes building intermediate-size machines of 127 and 433 qubits in 2021 and 2022, respectively, and envisions following up with a million-qubit machine at some unspecified date. Dario Gil, IBM's director of research, says he is confident his team can keep

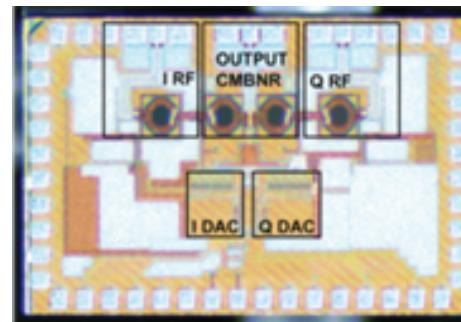
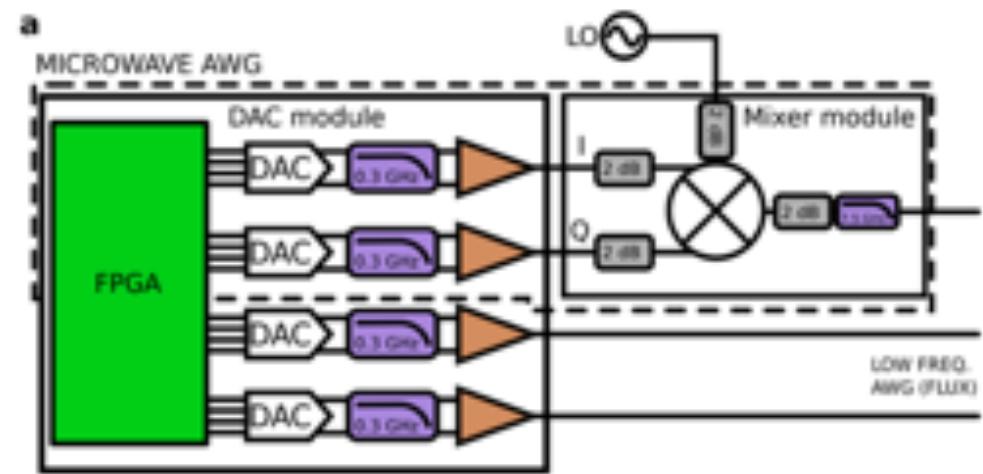
**Chinese researchers expect quantum leap in computing,
challenging Google's supremacy**

Global Times 8/26/2020

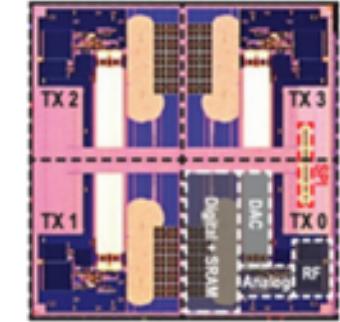
Chinese quantum computing researchers recently disclosed that a 50-qubit superconductivity quantum computing system with 99.5 percent fidelity could be achieved this year, and in 10 years, the system could evolve into a million-qubit level with a 99.8 percent fidelity, equivalent to, if not better than, its Google counterpart.

Microwave Challenges: Control of 1M qubit QC

- 1M XY and 3M baseband AWGs required
- Thermalize at 4K? 300? Other?
 - 4K simplifies system design, but <1mW/qubit required.
 - 300K eases power, but interconnects become harder.
- XY Challenges:
 - Frequency planning
 - Synchronization
 - Crosstalk
 - Waveform optimization
- Z Challenges:
 - Reflections
 - Z-tails (requires extensive calibration)
 - IR drops



Bardin, ISSCC, 2019
2mW/qubit XY controller @3K

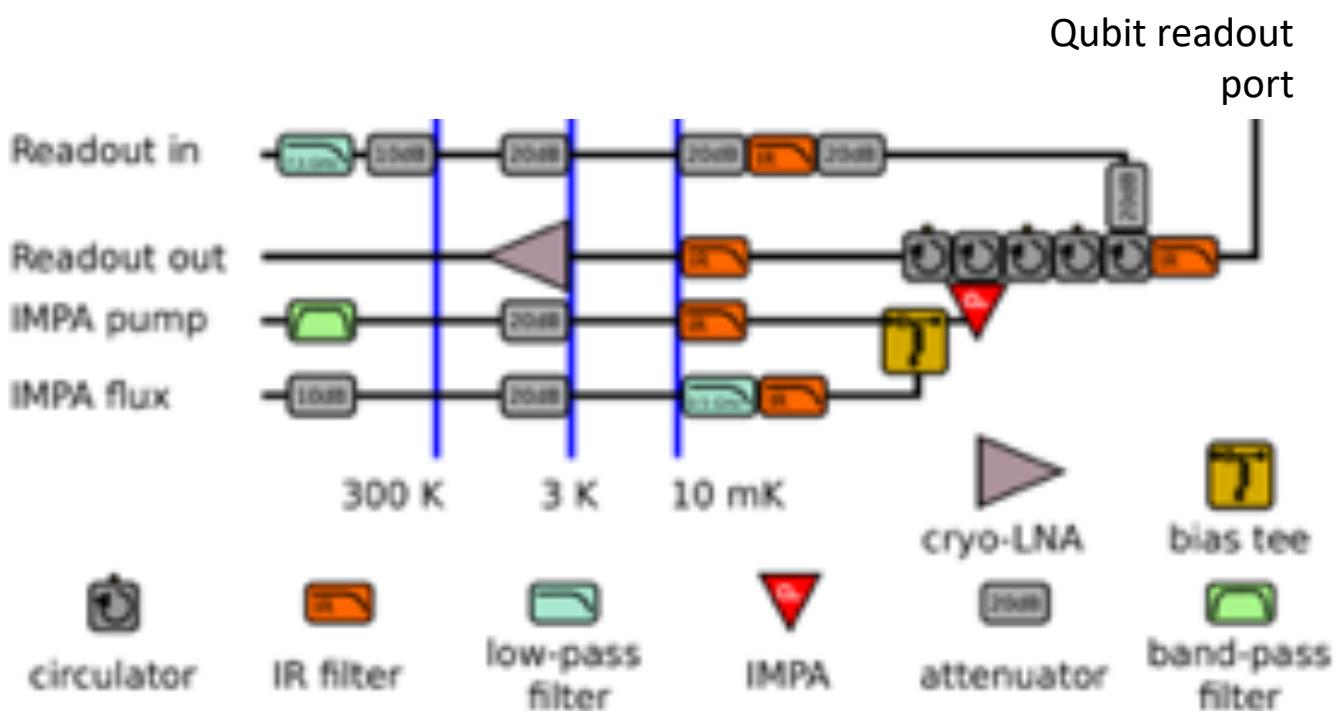


Patra, ISSCC, 2020
12mW/qubit 4x32 FDM
XY controller @3K

Microwave Challenges: Readout of 1M qubit QC

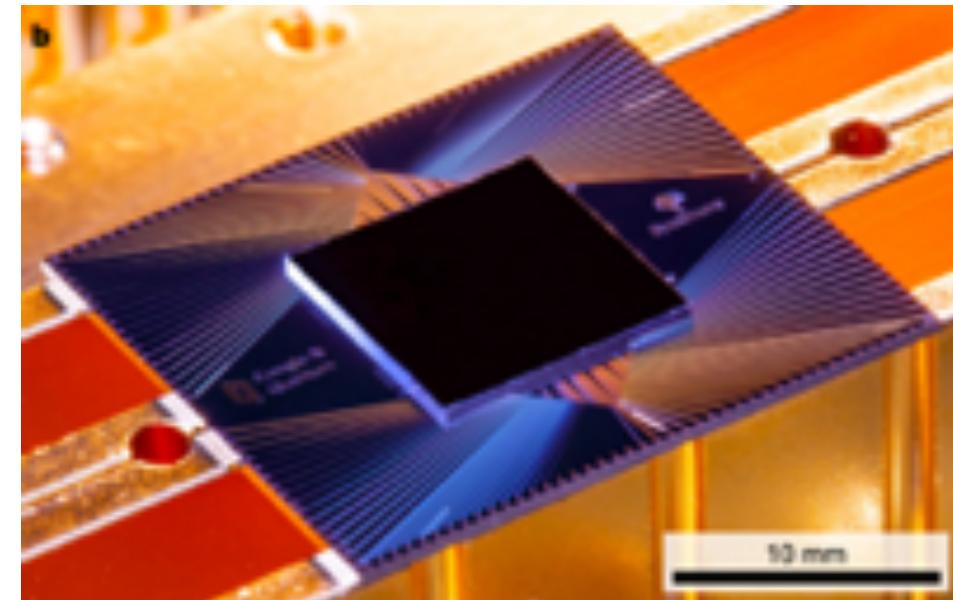
- Cryogenic semiconductor LNAs
 - Today: III-V amplifiers, individually tested, several mW
 - Future: Silicon amplifiers, spot tested, μW power?
- Parametric LNAs and 10mK passives
 - Par amps still ‘boutique technology’: needs maturation to improve yield
 - Currently several circulators per readout channel: either remove need or determine method to miniaturize

$\sim 100,000$ near quantum limited readout channels



Microwave Challenges: Interconnects & Packaging

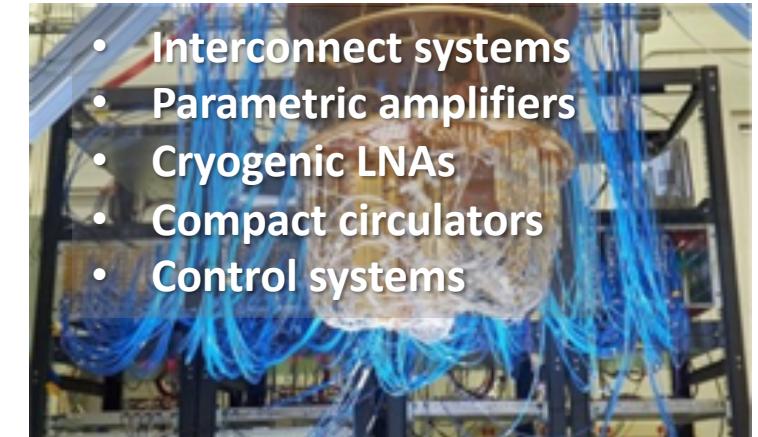
- Interconnects for 1M qubit QC:
 - 1M qubits => ~4.2M interconnects
 - High density (order mm pitch)
 - High thermal isolation
 - Superconducting from 4K to 10mK
 - Minimum thermal loading from room temp to 3K
 - Cross-talk must be controlled
- Package for 1M qubit QC:
 - ~5,000 interconnects per package/module
 - Very low RF loss
 - No relevant moding
 - High isolation
 - Excellent matching
 - Low contact resistance to package
 - EM simulation of packaged processor



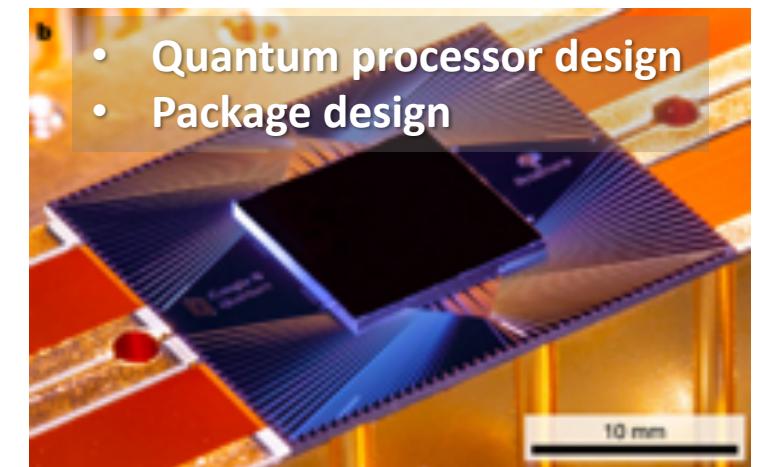
Conclusions

- Quantum computing largely enabled by microwave technologies
- Today's systems can already operate beyond the limits of classical computers for certain tasks
- Microwave engineers will be essential in the quest to implement a fault-tolerant quantum computer!
- Want to contribute? Please reach out!
bardinj@google.com
jbardin@engin.umass.edu

**Many opportunities for
μW engineers!**



- Interconnect systems
- Parametric amplifiers
- Cryogenic LNAs
- Compact circulators
- Control systems



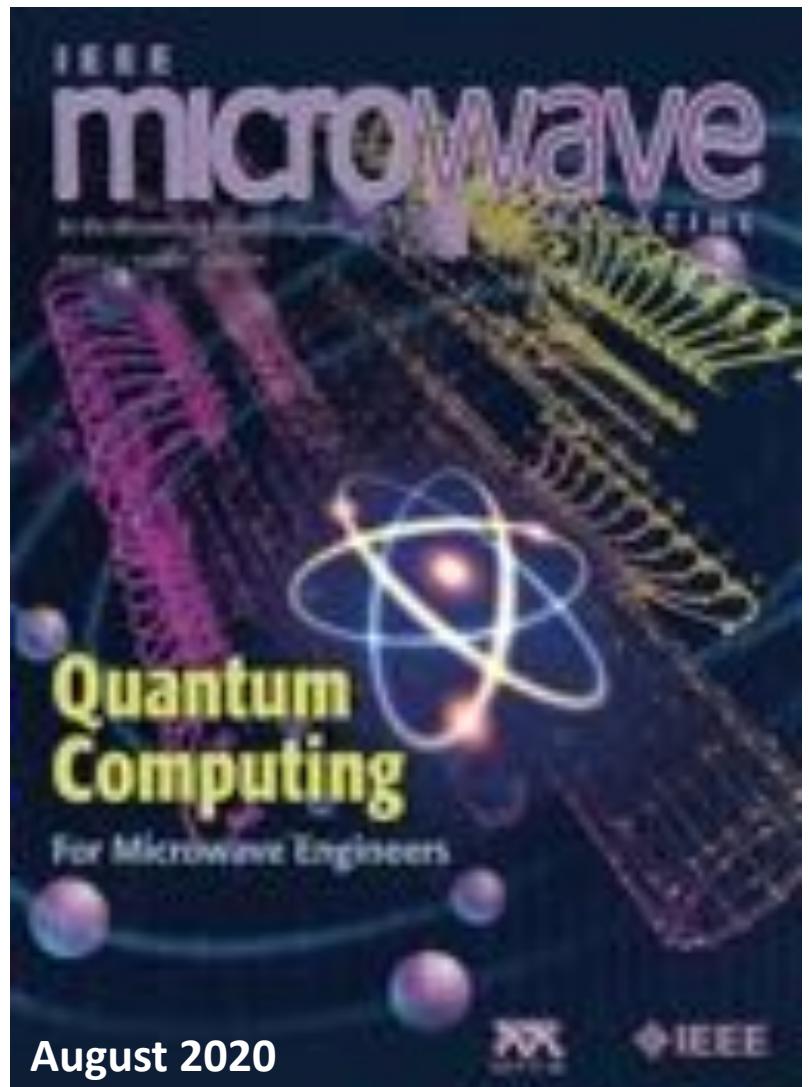
- Quantum processor design
- Package design



Google AI Quantum

Dong An, Frank Arute, Kunal Arya, Juan Atalaya, Ryan Babbush, Dave Bacon, Joseph Bardin, Rami Barends, Andreas Bengtsson, Alexander Bilmes, Sergio Boixo, Gina Bortoli, Alexandre Bourassa, Michael Broughton, Bob Buckley, David Buell, Brian Burkett, Nicholas Bushnell, Jimmy Chen, Yu Chen, Ben Chiaro, Jeremiah Coleman, Roberto Collins, Benjamin Correa, William Courtney, Sean Demura, Alan Derk, Andrew Dunsworth, Daniel Eppens, Catherine Erickson, Edward Farhi, Austin Fowler, Brooks Foxen, Craig Gidney, Marissa Giustina, Rob Graff, Amy Greene, Steve Habegger, Matthew Harrigan, Alan Ho, Markus Hoffmann, Sabrina Hong, Hsin-Yuan Huang, Trent Huang, Bill Huggins, Lev Ioffe, Sergei Isakov, Evan Jeffrey, Zhang Jiang, Cody Jones, Dvir Kafri, Kostyantyn Kechedzhi, Julian Kelly, Anton Khavluk, Seon Kim, Paul Klimov, Alexander Korotkov, Fedor Kostritsa, David Landhuis, Pavel Laptev, Lily Laws, Mike Lindmark, Erik Lucero, Orion Martin, Antonio Martinez, John Martinis, Sam McArdle, Jarrod McClean, Trevor McCourt, Matt McEwen, Anthony Megrant, Xiao Mi, Masoud Mohseni, Wojciech Mruczkiewicz, Josh Mutus, Ofer Naaman, Matthew Neeley, Charles Neill, Hartmut Neven, Michael Newman, Murphy Niu, Thomas O'Brien, Alex Opremcak, Eric Ostby, Sahil Patel, Bálint Pató, Andre Petukhov, Harald Putterman, Chris Quintana, Pedram Roushan, Nicholas Rubin, Daniel Sank, Kevin Satzinger, Vadim Smelyanskiy, Doug Strain, Kevin Sung, Marco Szalay, Matt Trevithick, Amit Vainsencher, Theodore White, Jamie Yao, Ping Yeh, Adam Zalcman, Harry Zhou, Alexander Zlokapa

More details



MANOJ KUMAR, PETER JAGGI, YUQI LIU, JAMES T. REED, ROBERT WILSON, JEFFREY A. COLE,
CHRISTOPHER D. BREWER, AND
HAROLD H. HARRIS

Microwaves in Quantum Computing

Joseph S. Brooks¹, Member, IEEE, Daniel M. Bierman², Member, IEEE,
Harold H. Harris³, Member, IEEE, and David J. Reilly⁴, Member, IEEE

¹Department of Electrical and Computer Engineering, University of Massachusetts Amherst, Amherst, MA 01003, USA
(✉) e-mail: brooks@ecs.umass.edu

²Quantum Optics Division, National Institute of Standards and Technology, Boulder, CO 80303, USA
(✉) e-mail: bierman@boulder.nist.gov

³Department of Electrical and Computer Engineering, University of Massachusetts Amherst, Amherst, MA 01003, USA
<http://www.ece.umass.edu/~harris/>
(✉) e-mail: harris@ecs.umass.edu

(Received Paper)

DOI: 10.1109/TMCOM.2020.2994256
This work was partially supported by the National Science Foundation Quantum Information Project.

ABSTRACT Quantum information processing systems rely on a broad range of microwave technologies and have spurred development of microwave devices and methods in new operating regimes. Here we review the use of microwave signals and techniques in quantum computing, with specific reference to three leading quantum computing platforms: trapped ions, superconducting qubits, and atom interferometers, and corresponding paths. We highlight recent key results and progress in quantum computing informed through the use of microwave systems, and discuss how quantum computing applications have pushed the envelope of microwave technology to new areas. We also discuss open microwave engineering challenges for the construction of large-scale, fault-tolerant quantum computers.

INDEX TERMS Semiconductor spin qubits, superconducting qubits, trapped ion qubits, quantum computing, gate control, gate noise, quantum classical interface.

End of presentation